Maximum Input 74.5V / 0.5A / Fixed Output 3.3V, 5V or 12V

DESCRIPTION

The FDSM series of the MagI³C Power Module family is a fixed output voltage, fully integrated DC-DC power supply including the controller IC, inductor and capacitors all in one package.

For optimal performance the module is recommended for use with an external input capacitor, reducing design effort and complexity to a minimum.

The FDSM ensures fast time to market and low development costs.

The module is specially suited for high transient 48V bus applications such as industrial motor drive control systems.

48V to 3.3V conversion achieves up to 89%. 48V to 5V conversion achieves up to 93%. 48V to 12V conversion achieves up to 95% efficiency.

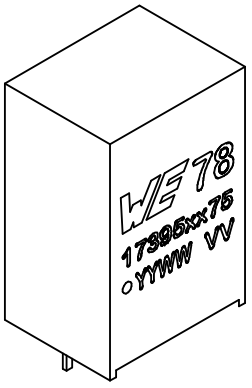
The standard THT (11.5 x 9 x 17.5)mm package allows for easy assembly.

TYPICAL APPLICATIONS

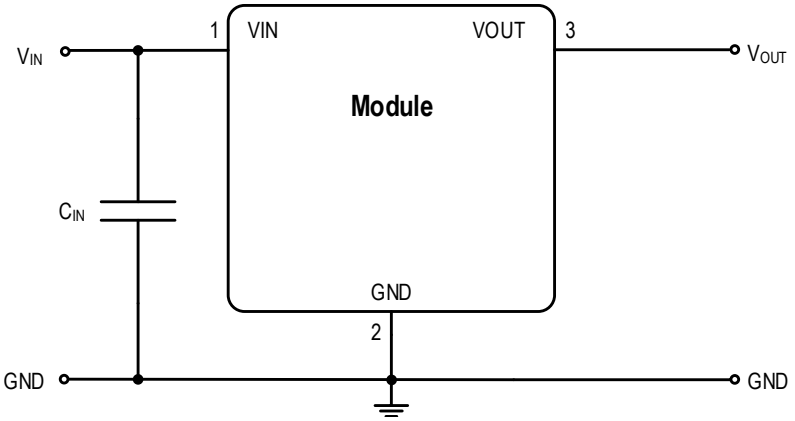
- Point-of-Load DC-DC applications
- Replacement for linear regulators
- Interface and microcontroller supplies
- General purpose
- 48V rail logistics & automation applications

FEATURES

- Peak efficiency up to 95%
- Current capability up to 0.5A
- Input voltage up to 74.5V
- Minimum input voltage / output voltage:
  - 9V / 3.3V (173950375)
  - 9V / 5V (173950575)
  - 18V / 12V (173951275)
- No minimum load required
- Partially integrated input and output capacitors
- Integrated inductor
- Low output voltage ripple (<50mV$_{pp}$)
- Current mode control
- Internal soft-start
- Thermal shutdown
- Short circuit protection
- Cycle by cycle current limit
- Pin compatible with the FDSM power modules series
- Ambient temperature range: -40°C to 85°C
- RoHS & REACh compliant
- Case and potting material UL 94 (flammability testing) certified
- Complies with EN55032 (CISPR-32) class B conducted and radiated emissions standard



TYPICAL CIRCUIT DIAGRAM



CONTENTS

1 PINOUT (PACKAGE TYPE B)

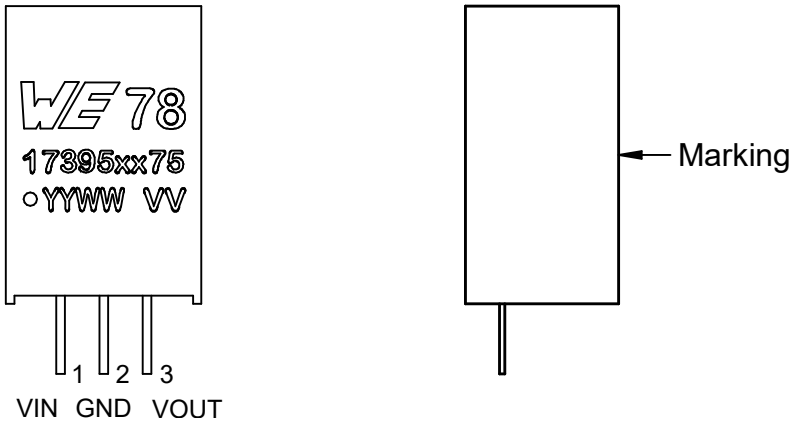


Figure 1: Pinout type B package (pins are located at the back).

Table 1: Marking description.

MARKING	DESCRIPTION
WE	Würth Elektronik eiSos GmbH & Co. KG
78	Indicates compatibility with 78xx linear regulator
17395xx75	Order code
YY	Year
WW	Calendar week
VV	Output voltage (3.3V, 5V or 12V)

Table 2: Pin description.

SYMBOL	NUMBER	TYPE	DESCRIPTION
VIN	1	Power	The supply input pin is a terminal for an input voltage source. It is recommended to use 2x10µF/100V input capacitors.
GND	2	Power	Ground pin; reference for V_{IN} and V_{OUT}.
VOUT	3	Power	Regulated output voltage pin. There is no need for an external output capacitor.

2 ORDERING INFORMATION

ORDER CODE	SPECIFICATIONS	PACKAGE	PACKAGING UNIT
173950375	74.5V / 0.5A / 3.3Vout version	Type B	Tube with 43 pieces
173950575	74.5V / 0.5A / 5Vout version		
173951275	74.5V / 0.5A / 12Vout version		

3 PINOUT COMPATIBLE FAMILY MEMBERS

ORDER CODE	SPECIFICATIONS	PACKAGE	PACKAGING UNIT
173950378	28V / 500mA / 3.3Vout	Type F	Tube with 42 pieces
173950578	28V / 500mA / 5Vout		
173010378	28V / 1A / 3.3Vout		
173010578	28V / 1A / 5Vout		
173010342	42V / 1A / 3.3Vout		
173010542	42V / 1A / 5Vout		
173950336	36V / 500mA / 3.3Vout		Tube with 43 pieces
173950536	36V / 500mA / 5Vout		
173951236	36V / 500mA / 12Vout		
173951536	36V / 500mA / 15Vout		
173010335	36V / 1A / 3.3Vout		
173010535	36V / 1A / 5Vout		
173011235	36V / 1A / 12Vout		
173011535	36V / 1A / 15Vout		

4 SALES INFORMATION

SALES CONTACT
Würth Elektronik eiSos GmbH & Co. KG EMC and Inductive Solutions Max-Eyth-Str. 1 74638 Waldenburg Germany Tel. +49 (0) 7942 945 0 www.we-online.com/powermodules Technical support: powermodules@we-online.com

5 ABSOLUTE MAXIMUM RATINGS

Caution:
Exceeding the listed absolute maximum ratings may affect the device negatively and may cause permanent damage.

Table 3: Absolute maximum ratings.

SYMBOL	PARAMETER	LIMIT		UNIT
		MIN(1)	MAX(1)	
VIN	Input pin voltage 17395xx75	-0.3	90	V
VOUT	Output pin voltage 3.3Vout / 5Vout version	-0.3	16	V
	Output pin voltage 12Vout version	-0.3	25	V
$T_{storage}$	Assembled, non-operating storage temperature	-40	125	° C
V_{esd}	ESD Voltage (Human Body Model), according to EN61000-4-2 (4)	-4	4	kV

6 OPERATING CONDITIONS

Operating conditions are conditions under which the device is intended to be functional. All values are referenced to GND.

MIN and MAX limits are valid for the recommended ambient temperature range of -40°C to 85°C. Typical values represent statistically the utmost probable values at the following conditions: V_{IN}= 9V to 74.5V (173950375), V_{IN}= 9V to 74.5V (173950575), V_{IN}= 18V to 74.5V (173951275), I_{OUT}= 0.5A, T_A = 25 °C, unless otherwise noted.

Table 4: Operating conditions.

SYMBOL	PARAMETER	MIN(1)	TYP(3)	MAX(1)	UNIT
V_{IN}	Input Voltage (173950375)	9	-	74.5	V
V_{IN}	Input Voltage (173950575)	9	-	74.5	V
V_{IN}	Input Voltage (173951275)	18	-	74.5	V
T_a	Ambient temperature range	-40	-	85 (2)	°C
I_{OUT}	Nominal output current(5)	-	-	0.5	A
$C_{OUT\ MAX}$	Maximal output capacitance	-	-	100	µF

7 ELECTRICAL SPECIFICATIONS

Caution:
MIN and MAX limits are valid for the recommended ambient temperature range of -40°C to 85°C. Typical values represent statistically the utmost probable values at the following conditions: V_{IN}= 48V (173950375, 173950575 and 173951275), I_{OUT}= 0.5A, T_A = 25°C, unless otherwise noted.

Table 5: Electrical specifications.

SYMBOL	PARAMETER	TEST CONDITIONS	LIMIT			UNIT
			MIN(1)	TYP(3)	MAX(1)	
Output Current						
I_{CL}	Current limit threshold	V_{OUT} = 3.3	-	1.1	-	A
		V_{OUT} = 5	-	1.2	-	A
		V_{OUT} = 12	-	0.9	-	A
Output Voltage						
V_{OUT}	Regulated output voltage	173950375	-	3.3	-	V
		173950575	-	5	-	V
		173951275	-	12	-	V
	Line regulation	I_{OUT} = 0.5A	-0.4	±0.2	0.4	%
	Load Regulation	10% to 100% load	-0.6	±0.4	0.6	%
	Total output voltage regulation	V_{OUT} = 3.3V, I_{OUT} = 0.5A	-4.5	±3.5	4.5	%
		V_{OUT} = 5V / 12V, I_{OUT} = 0.5A	-3	±2	3	%
	External 2x C_{OUT} = 10µF, 25V, X5R, 20MHz BWL(6)					
	Output voltage ripple	3.3V, 0.5A	-	40	-	mV_{pp}
		5V / 12V, 0.5A	-	30	-	mV_{pp}
Switching Frequency						
f_{SW}	Switching frequency	V_{OUT} = 3.3V, I_{OUT} = 0.5A	-	166	-	kHz
		V_{OUT} = 5V, I_{OUT} = 0.5A	-	250	-	kHz
		V_{OUT} = 12V, I_{OUT} = 0.5A	-	400	-	kHz
Input Current						
I_{IN}	No load input current	Operating, switching	-	0.3	1	mA
Efficiency						
η	Efficiency, I_{OUT} = 0.5A	V_{IN} = 9V, V_{OUT} = 3.3V	-	89	-	%
		V_{IN} = 48V, V_{OUT} = 3.3V	-	81	-	%
		V_{IN} = 9V, V_{OUT} = 5V	-	92	-	%
		V_{IN} = 48V, V_{OUT} = 5V	-	86	-	%
		V_{IN} = 18V, V_{OUT} = 12V	-	95	-	%
		V_{IN} = 48V, V_{OUT} = 12V	-	91	-	%

8 RoHS, REACh

Table 6: RoHS, REACh.

RoHS directive	COMPLIANT RoHS&REACh WÜRTH ELEKTRONIK	Directive 2011/65/EU of the European Parliament and the Council of June 8th, 2011 on the restriction of the use of certain hazardous substances in electrical and electronic equipment.
REACh directive		Directive 1907/2006/EU of the European Parliament and the Council of June 1st, 2007 regarding the Registration, Evaluation, Authorization and Restriction of Chemicals (REACh).

9 RELIABILITY

Table 7: Reliability.

SYMBOL	PARAMETER	TEST CONDITIONS	TYP[3]	UNIT
MTBF	Mean time between failures	MIL-HDBK-217F, 25°C	$5000 \cdot 10^3$	h

10 PACKAGE SPECIFICATIONS

Table 8: Package specifications.

ITEM	PARAMETER	TYP[3]	UNIT
Case	Black flame-retardant and heat-resistant plastic (UL94 V-0)	-	-
Potting material	Silicone, UL94V-0	-	-
Weight		3.8	g
Vibration	5g for 20 min	MIL-STD-202, Method 204	

11 NOTES

(1) Min and Max limits are 100% production tested at 25°C. Limits over the operating temperature range are guaranteed through correlation using Statistical Quality Control (SQC) methods.

(2) Measured without heatsink. Natural convection (0 - 20LFM / 0- 0.1m/s) on the a 57 x 35mm two layer test board, with 35µm (1 ounce) copper.

(3) Typical numbers are valid at 25°C ambient temperature and represent statistically the utmost probable values assuming a Gaussian distribution.

(4) The human body model is a 100pF capacitor discharged through a 1.5Ωk resistor into each pin. Test method is per JESD-22-114.

(5) Depending on ambient temperature; see THERMAL DERATING.

(6) The industry standard for comparison of the output voltage ripple between switching regulators or modules requires a 10µF ceramic (sometimes additional 1µF ceramic in parallel) at the point of load where the voltage measurement is done using an oscilloscope with its probe and probe jack designed for low voltage/high frequency (low impedance) measurement. The oscilloscopes bandwidth is limited at 20MHz.

12 TYPICAL PERFORMANCE CURVES

If not otherwise specified, the following conditions apply: V_{IN} = 48V; V_{OUT} = 3.3V (173950375), V_{OUT} = 5V (173950575) and V_{OUT} = 12V (173951275); I_{OUT} = 0.5[(5)]; T_{AMB} = 25°C.

12.1 Radiated and Conducted Emissions EN55032 (CISPR-32) Class B Compliance

The 173950375, 173950575 and 173951275 power modules are tested with a standard EMC configuration (1m wire between the module and the load) to give more realistic information about implementation in the applications. The test setup is based on CISPR-16 with the limit values taken from CISPR-32.
Measured with module on the design example shown in chapter DESIGN EXAMPLE in a Fully Anechoic Room (FAR) at 3m antenna distance.

12.1.1 TEST SETUP

Input wire length:

- Radiated Emission: 160cm (80cm Horizontal + 80cm Vertical)
- Conducted Emission: 80cm

Output wire length:

- Radiated and Conducted Emissions:100cm Horizontal

12.1.2 Radiated and Conducted Emissions - 173950375

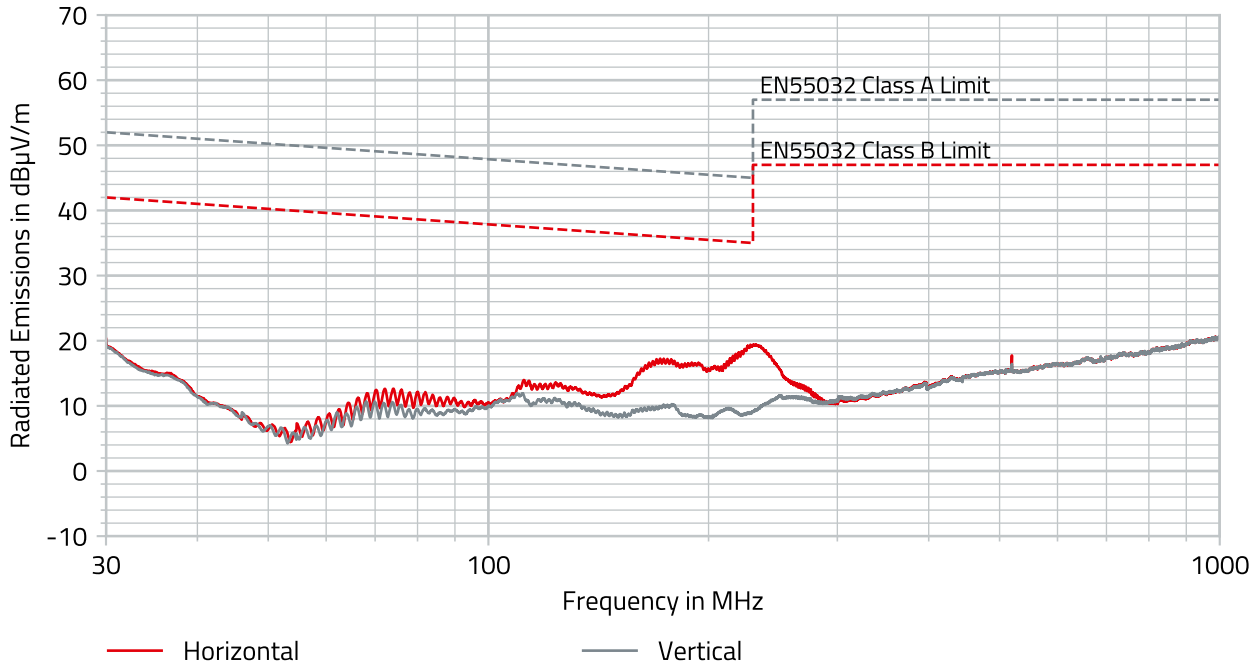


Figure 2: 173950375 radiated EMI V_{IN} = 48V, V_{OUT} = 3.3V, I_{OUT} = 0.5A with input filter.

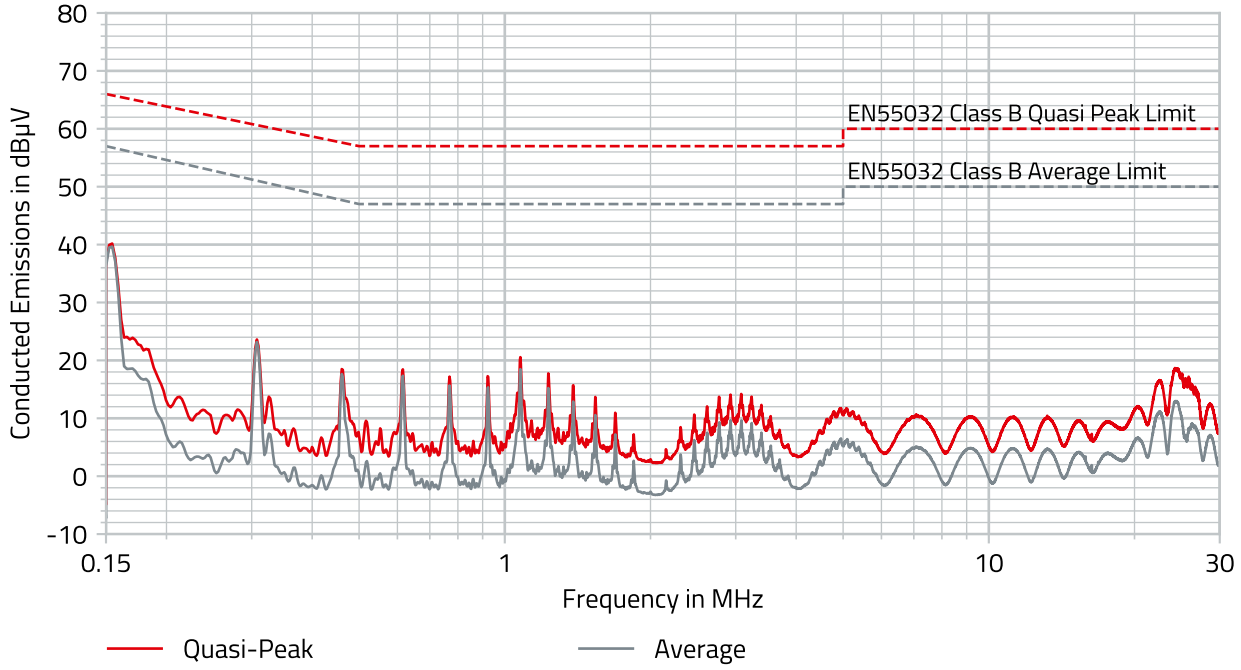


Figure 3: 173950375 conducted EMI V_{IN} = 48, V_{OUT} = 3.3, I_{OUT} = 0.5 with input filter.

12.1.3 Radiated and Conducted Emissions - 173950575

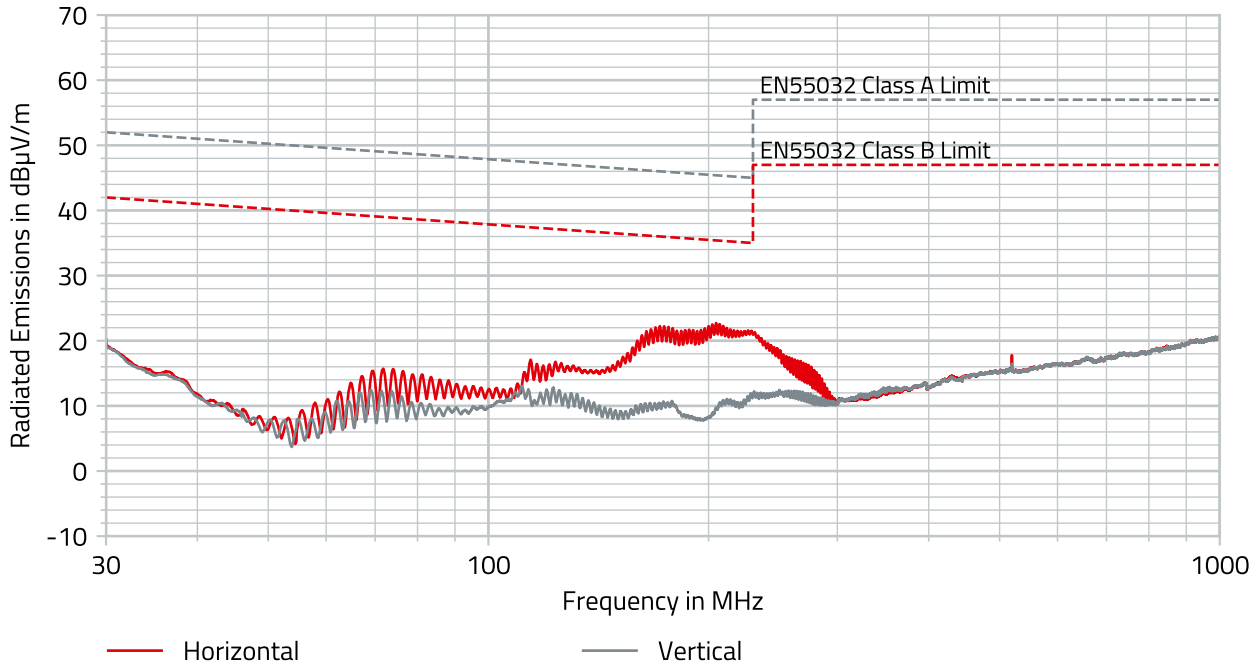


Figure 4: 173950575 radiated EMI V_{IN} = 48, V_{OUT} = 5, I_{OUT} = 0.5 with input filter.

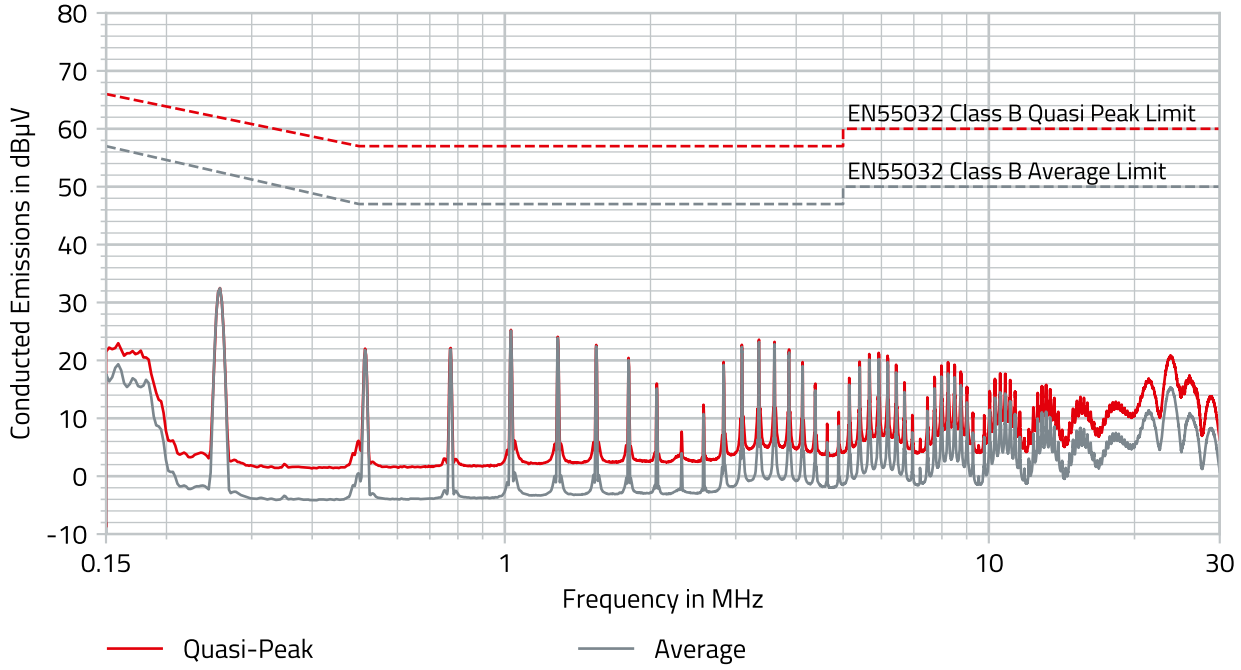


Figure 5: 173950575 conducted EMI V_{IN} = 48, V_{OUT} = 5, I_{OUT} = 0.5 with input filter.

12.1.4 Radiated and Conducted Emissions - 173951275

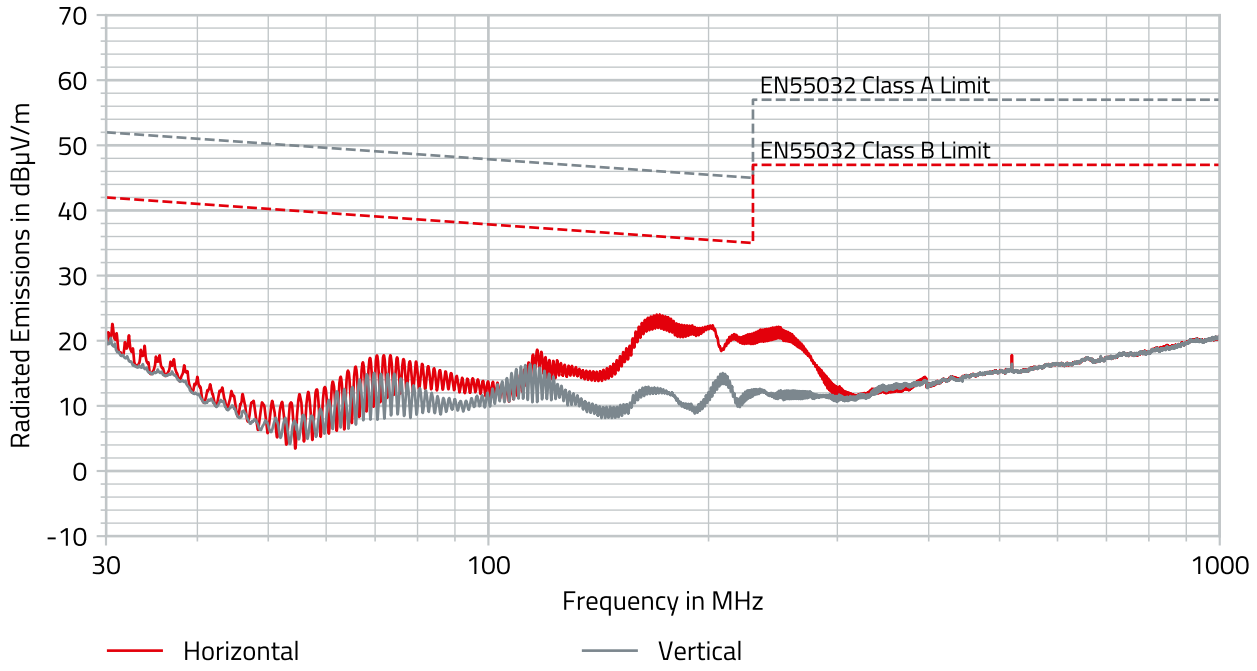


Figure 6: 173951275 radiated EMI V_{IN} = 48, V_{OUT} = 12, I_{OUT} = 0.5 with input filter.

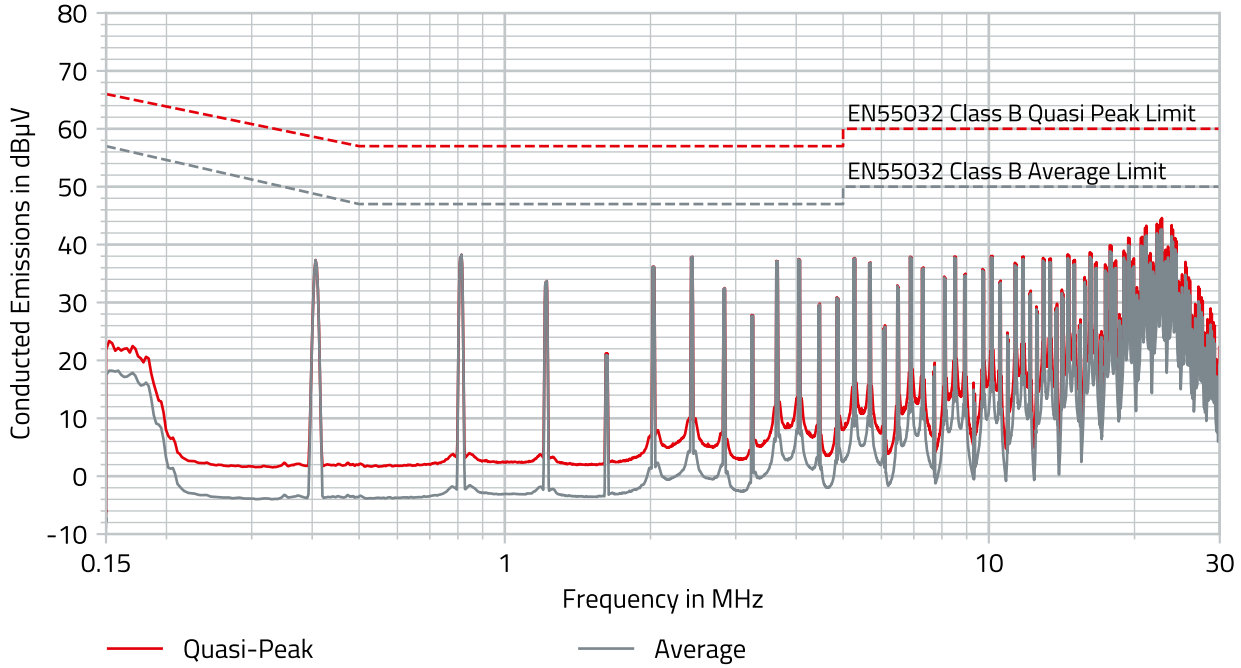


Figure 7: 173951275 conducted EMI V_{IN} = 48, V_{OUT} = 12, I_{OUT} = 0.5 with input filter.

12.2 DC PERFORMANCE CURVES

12.2.1 EFFICIENCY

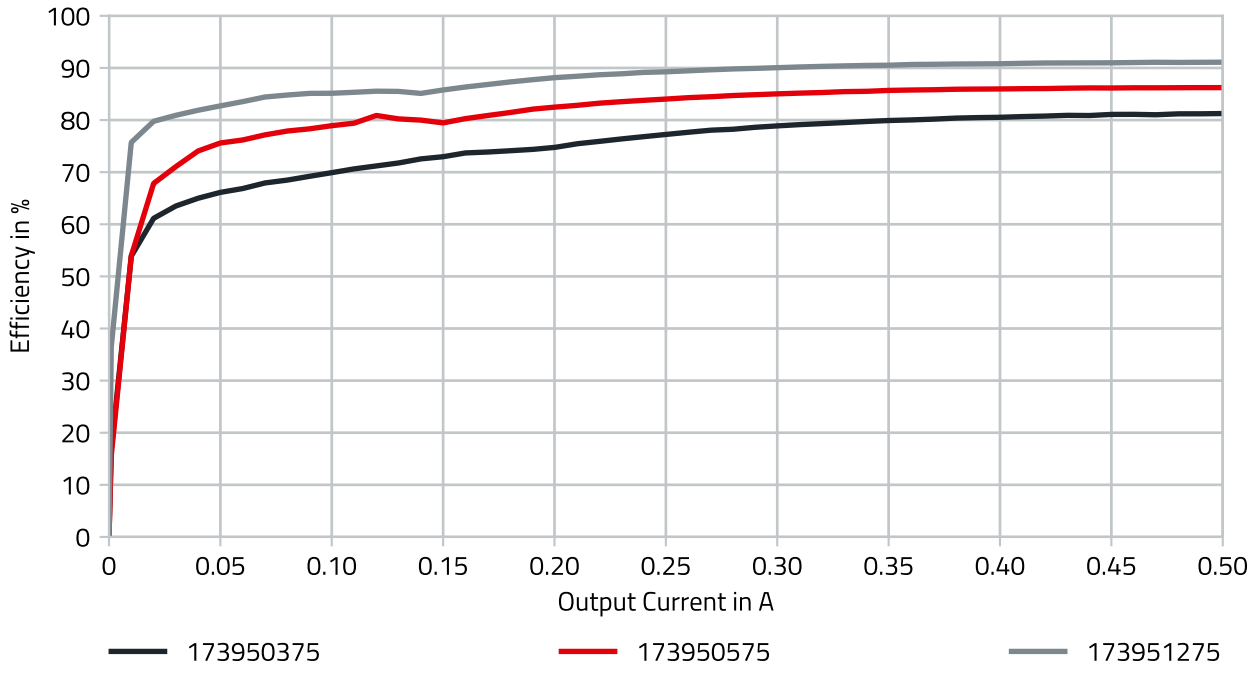


Figure 8: Efficiency.

12.2.2 THERMAL DERATING

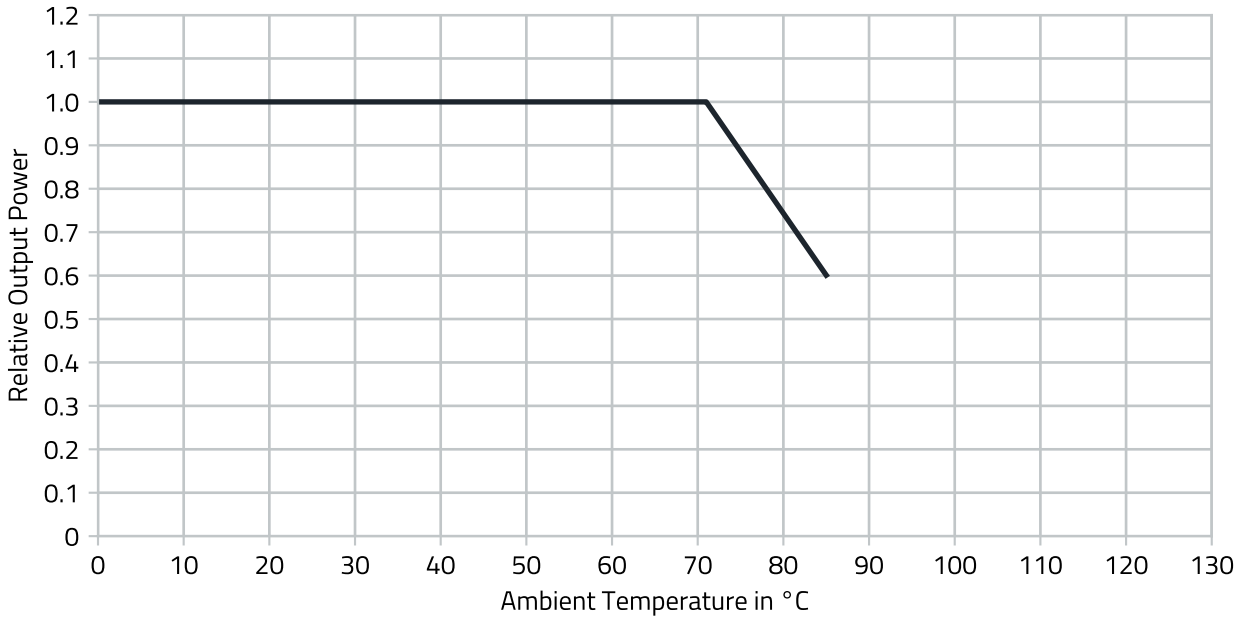


Figure 9: Thermal derating.

12.2.3 LOAD REGULATION

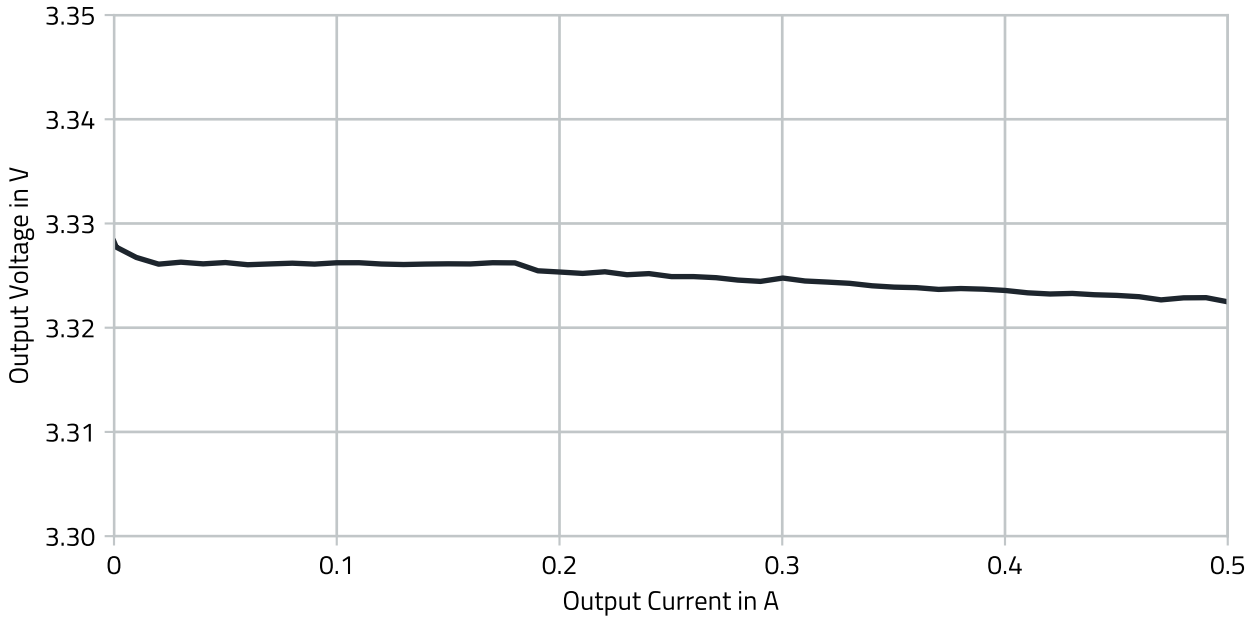


Figure 10: 173950375 load regulation.

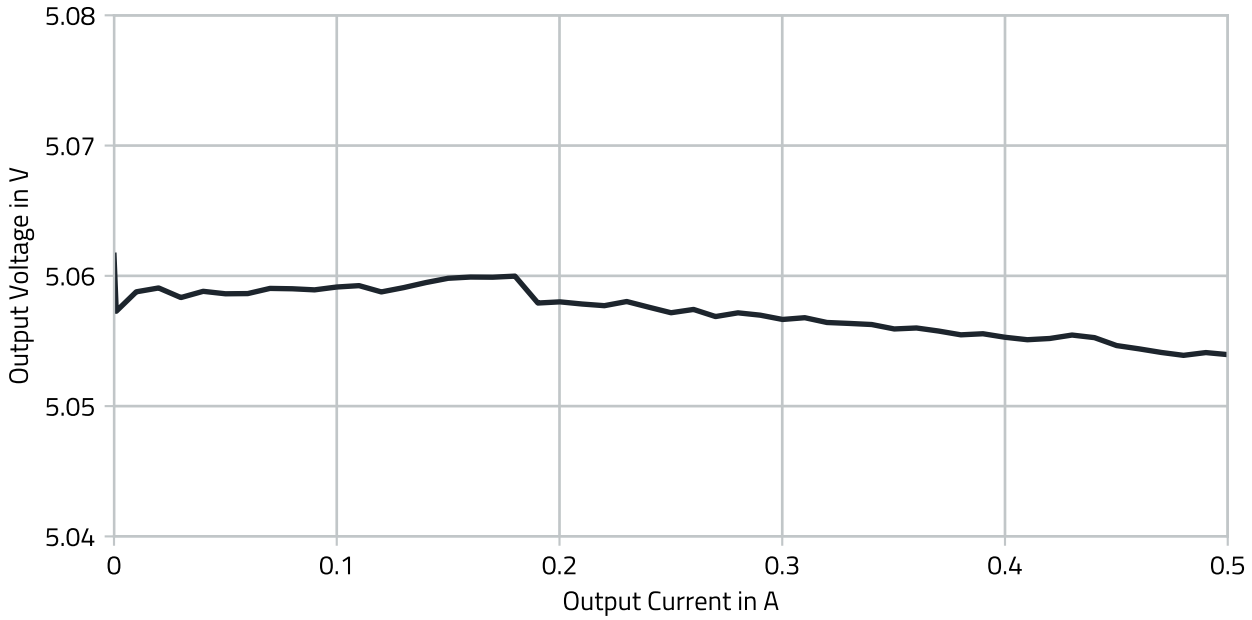


Figure 11: 173950575 load regulation.

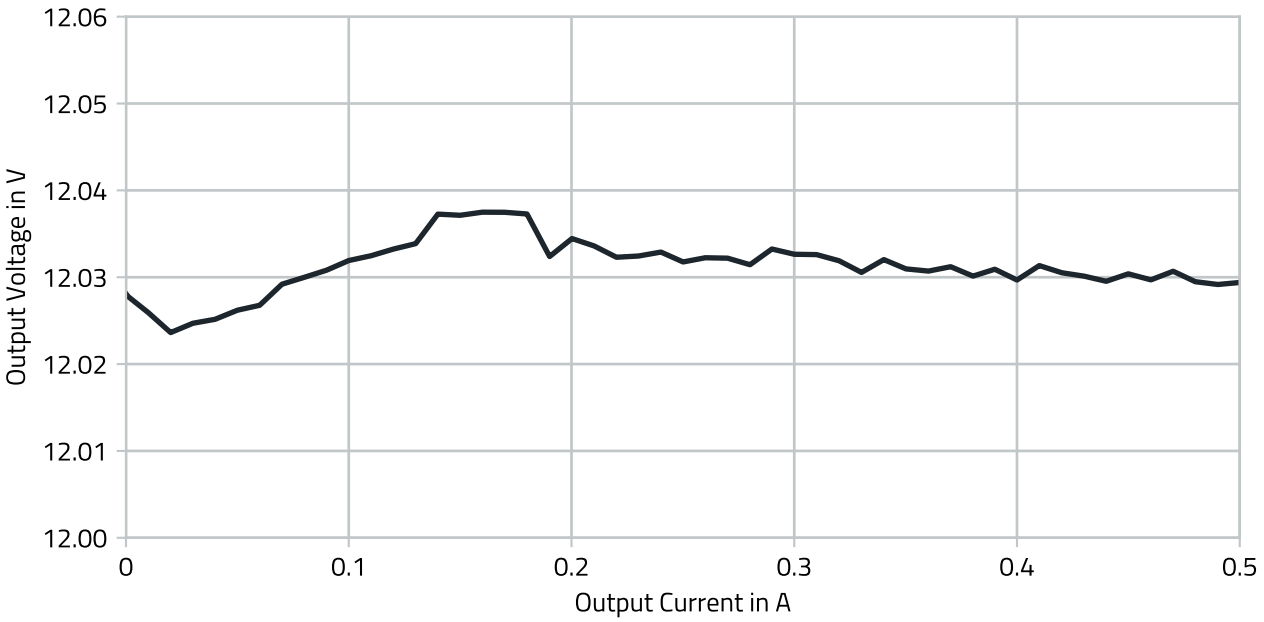


Figure 12: 173951275 load regulation.

12.2.4 LINE REGULATION

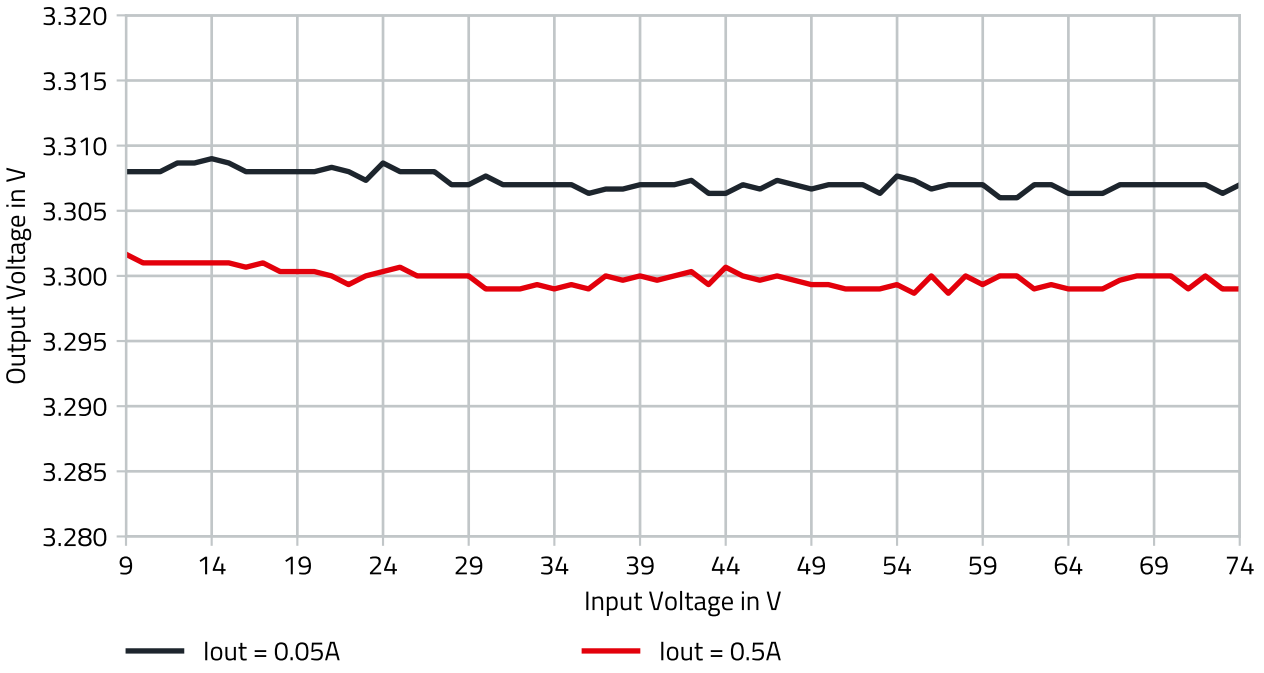


Figure 13: 173950375 line regulation.

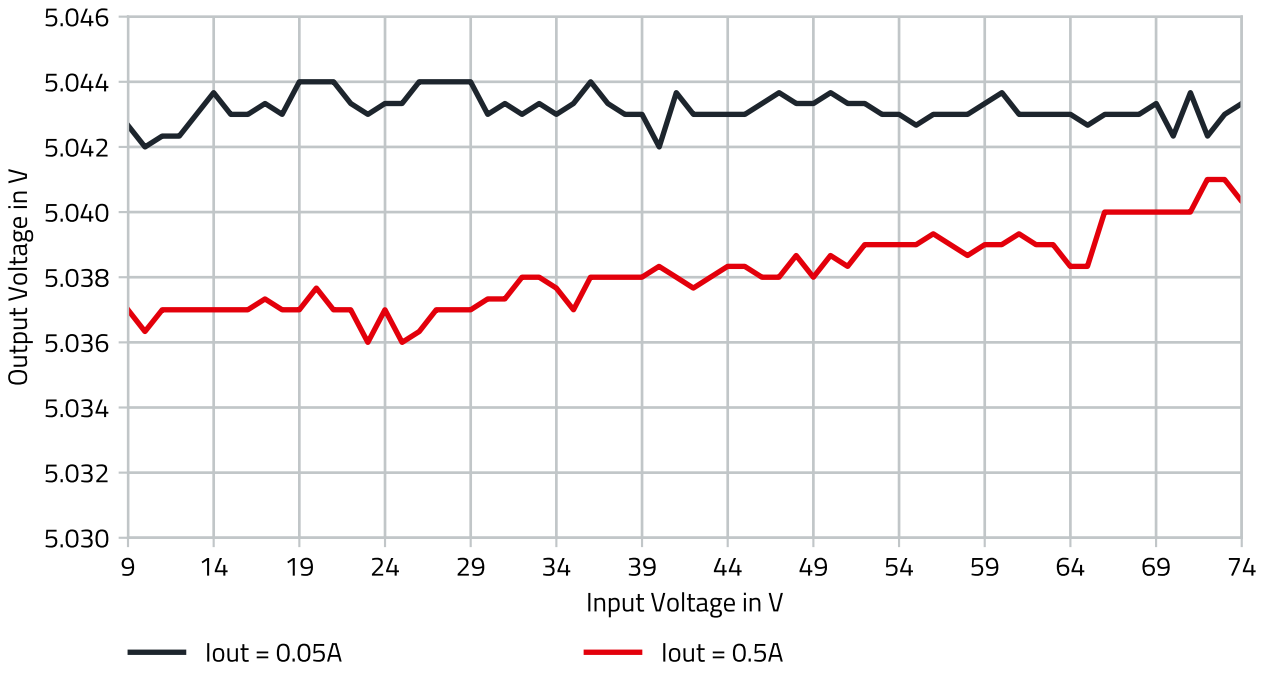


Figure 14: 173950575 line regulation.

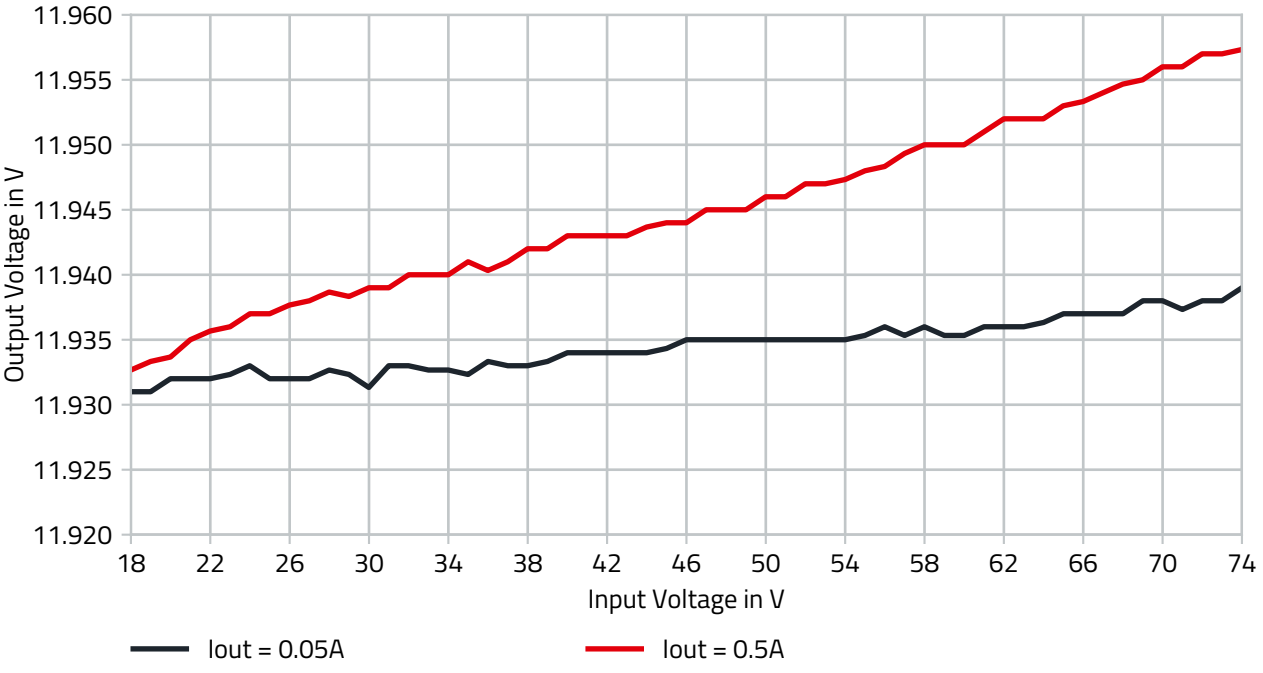


Figure 15: 173951275 line regulation.

13 BLOCK DIAGRAM

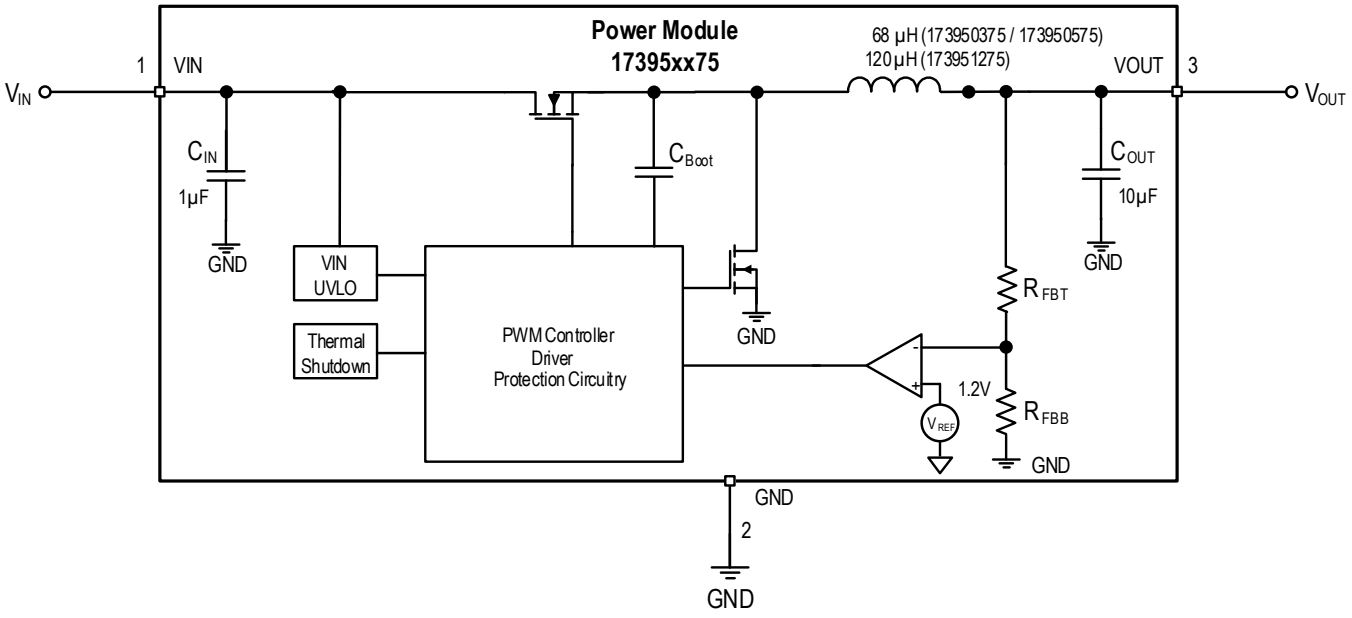


Figure 16: 17395xx75 block diagram.

14 CIRCUIT DESCRIPTION

The MagI³C power modules 17395xx75 are all synchronous step-down regulator with integrated IC, power inductor, input and output capacitors. The control scheme is based on a constant on-time (COT) regulation loop.

The V_{OUT} of the regulator is divided by the internal feedback resistor network and fed into the error amplifier, which compares this signal with the internal 1.2V reference. The error amplifier controls the on-time of a fixed frequency pulse width generator, which drives the MOSFET.

To achieve a regulated output voltage the off-time is modulated. It is stable with low ESR output capacitors. No external compensation network is required. This architecture supports fast transient response and very small output voltage ripple values (<50mV_{pp}) are achieved.

15 OUTPUT VOLTAGE RIPPLE

The output voltage ripple depends on several parameters. The figure below shows the V_{OUT} ripple at full load using no external output capacitor. An output voltage ripple of less than 50mV_{pp} is measured under the conditions indicated. To reduce the output ripple further it is recommended to add two 22µF MLCC output capacitor.

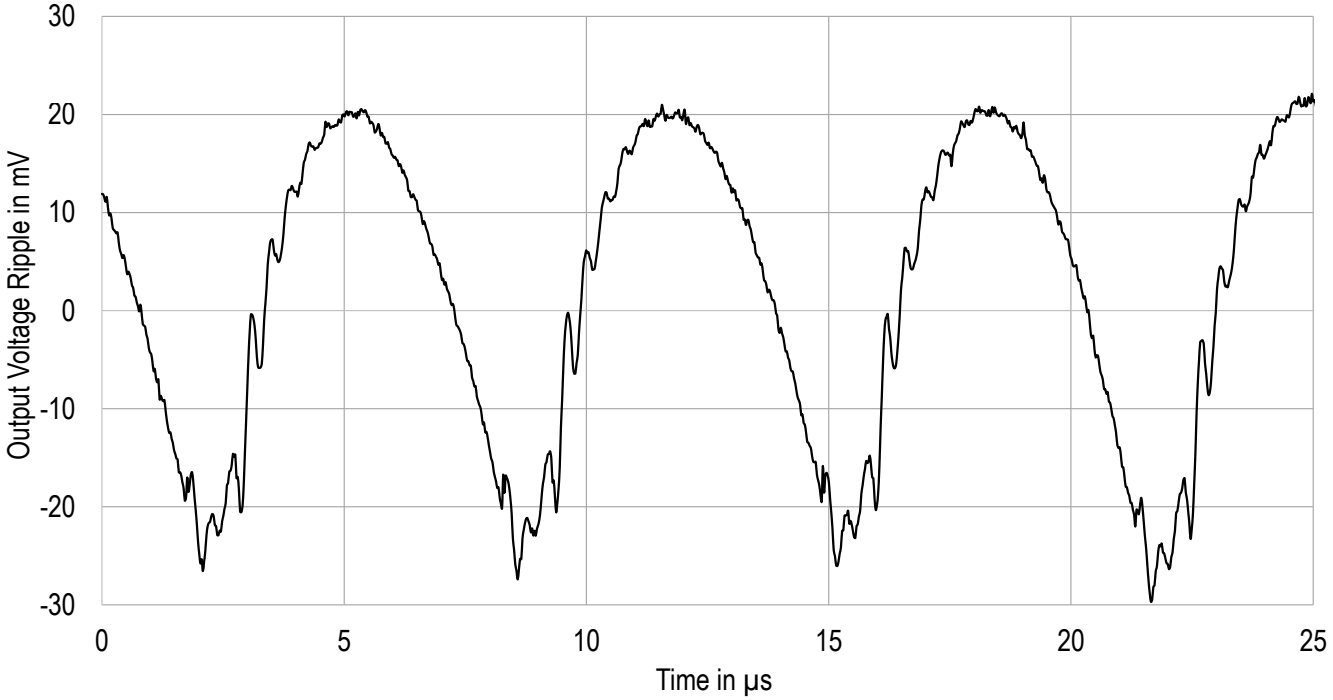


Figure 17: 173950375 output voltage ripple, V_{IN} = 48V, V_{OUT} = 3.3V, I_{OUT} = 0.5A.

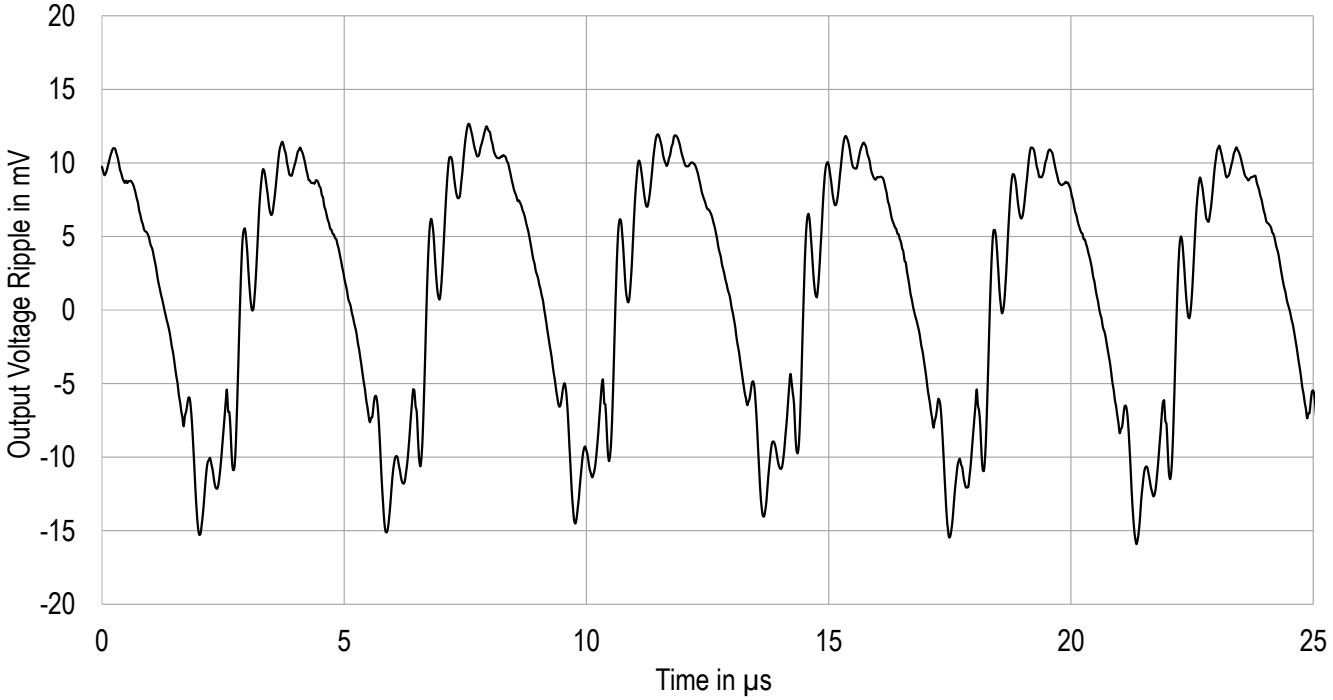


Figure 18: 173950575 output voltage ripple, V_{IN} = 48V, V_{OUT} = 5V, I_{OUT} = 0.5A.

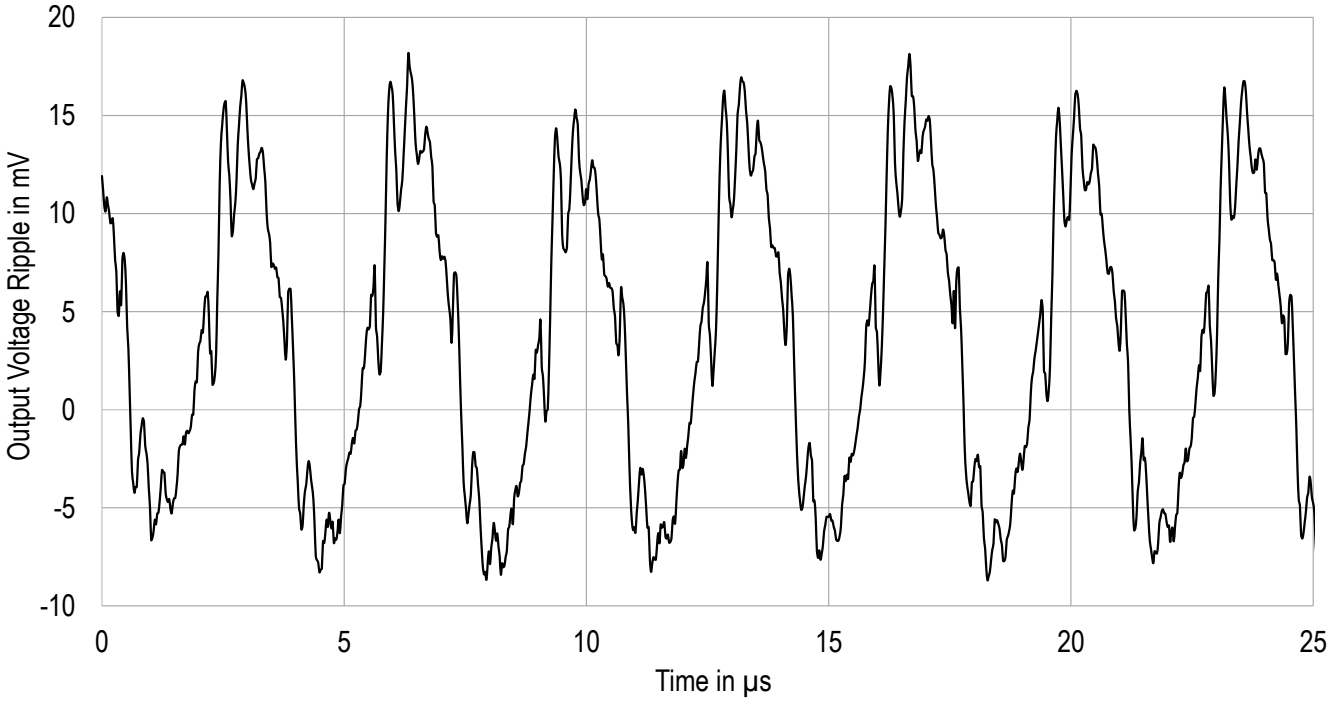


Figure 19: 173951275 output voltage ripple, V_{IN} = 48V, V_{OUT} = 12V, I_{OUT} = 0.5A.

16 PROTECTION FEATURES

16.1 Soft-Start

An approximately 2ms internal soft-start prevents the output voltage from overshooting during start up. The figures below show the start-up behavior of the power module with the 3.3V, 5V and 12V output voltage, respectively.

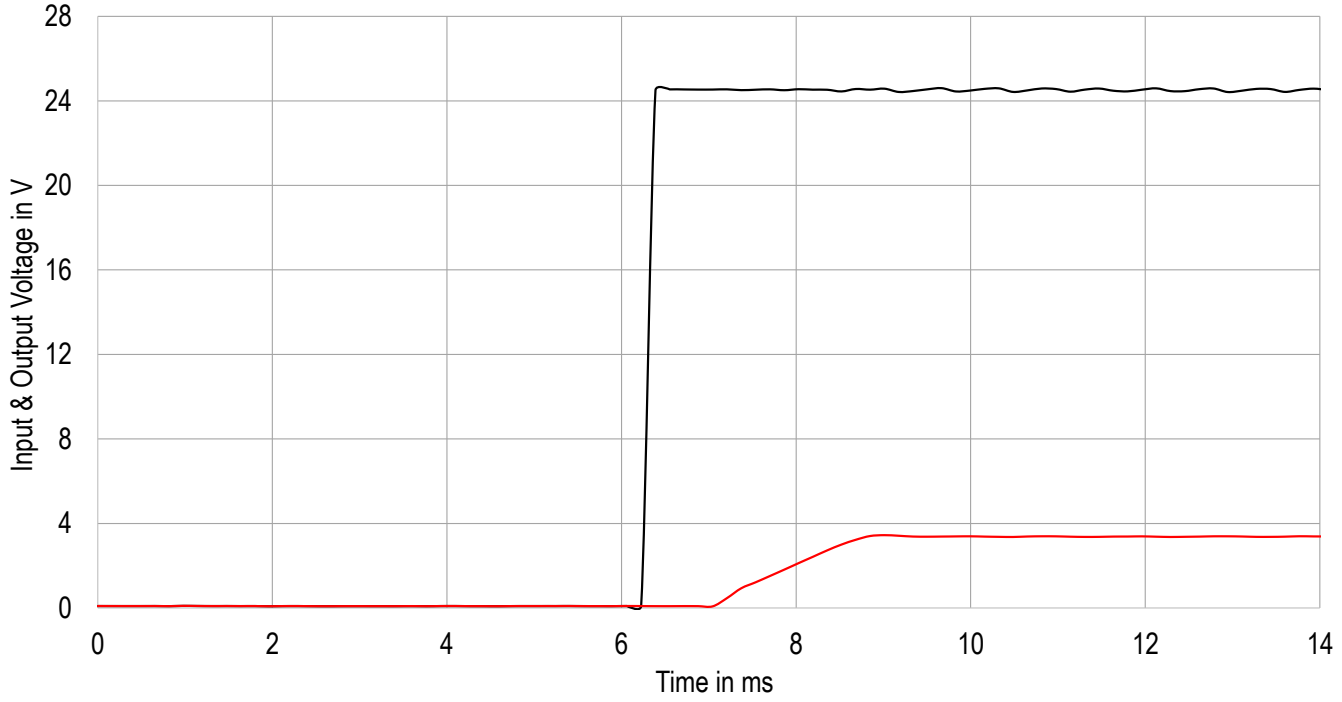


Figure 20: 173951275 soft-start, V_{IN} = 24V, V_{OUT} = 3.3V, I_{OUT} = 0.5A.

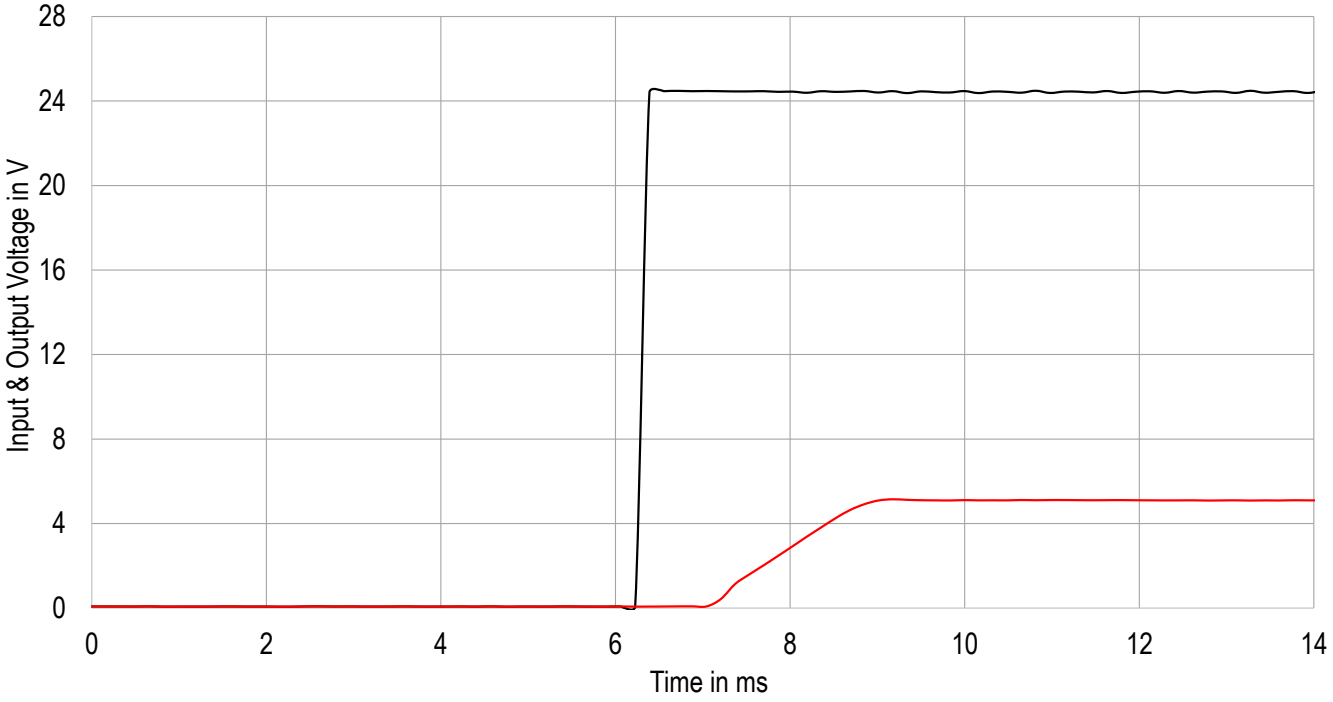


Figure 21: 173950375 soft-start, V_{IN} = 24V, V_{OUT} = 5V, I_{OUT} = 0.5A.

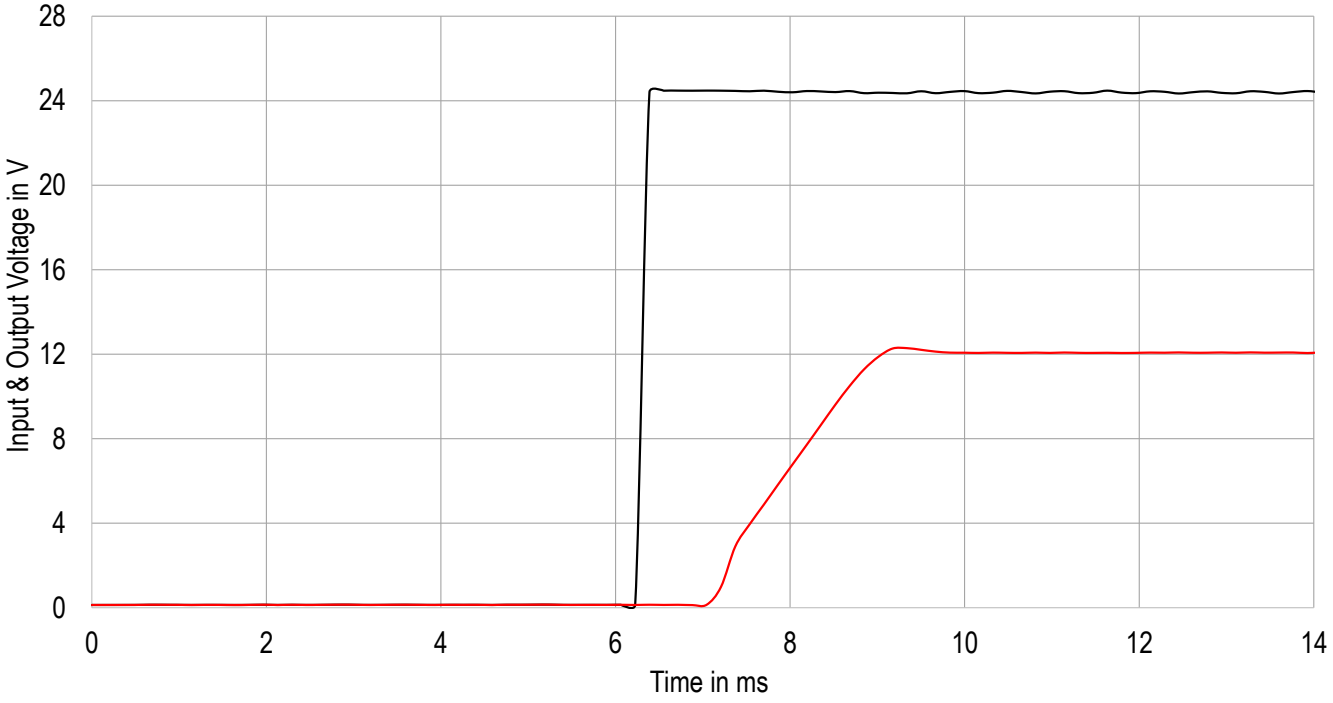


Figure 22: 173950575 soft-start, V_{IN} = 24V, V_{OUT} = 12V, I_{OUT} = 0.5A.

16.2 Over Temperature Protection (OTP)

Thermal protection helps to prevent catastrophic failures due to accidental device overheating. The junction temperature of the MagI³C power module should not be allowed to exceed its maximum ratings. Thermal protection is implemented by an internal thermal shutdown circuit which activates at 155° C (typ.), causing the device to enter a low power standby state. In this state, the MOSFET remains off, causing V_{OUT} to fall. When the junction temperature falls back below 125° C (typ.) (hysteresis is implemented) V_{OUT} rises smoothly and normal operation resumes.

16.3 Short Circuit Protection (SCP)

The short-circuit protection is realized via cycle by cycle current monitoring and a frequency foldback scheme where the off time of the high side switch increases relative to a decrease of the feedback voltage. For example, the power module switching frequency is divided by 2, 4 and 8 as the feedback voltage decreases to 75%, 50% and 25%, respectively. Recovery from short circuit protection mode occurs during the switching cycle following the removal of the short circuit condition. When the power module recovers from a short circuit condition, the soft-start will not activate. Therefore, it is possible that an overshoot at the output voltage can be observed. Under short circuit conditions, the input current is limited.

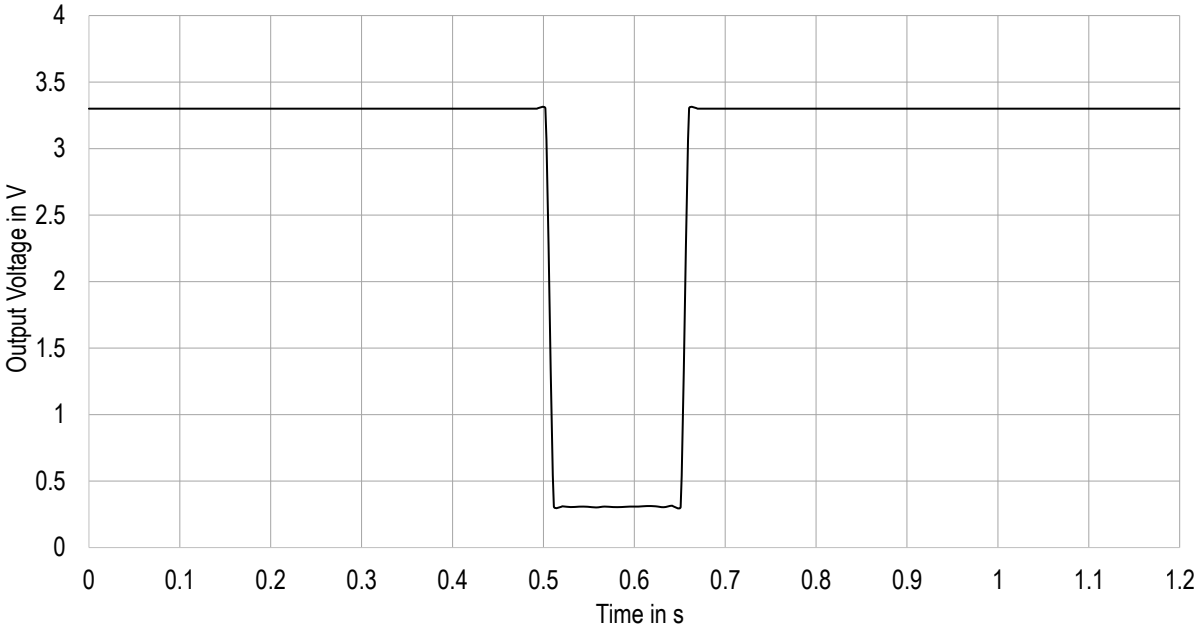


Figure 23: 173950375 short circuit protection, V_{IN} = 48V, V_{OUT} = 3.3V, I_{OUT} = 0.5A.

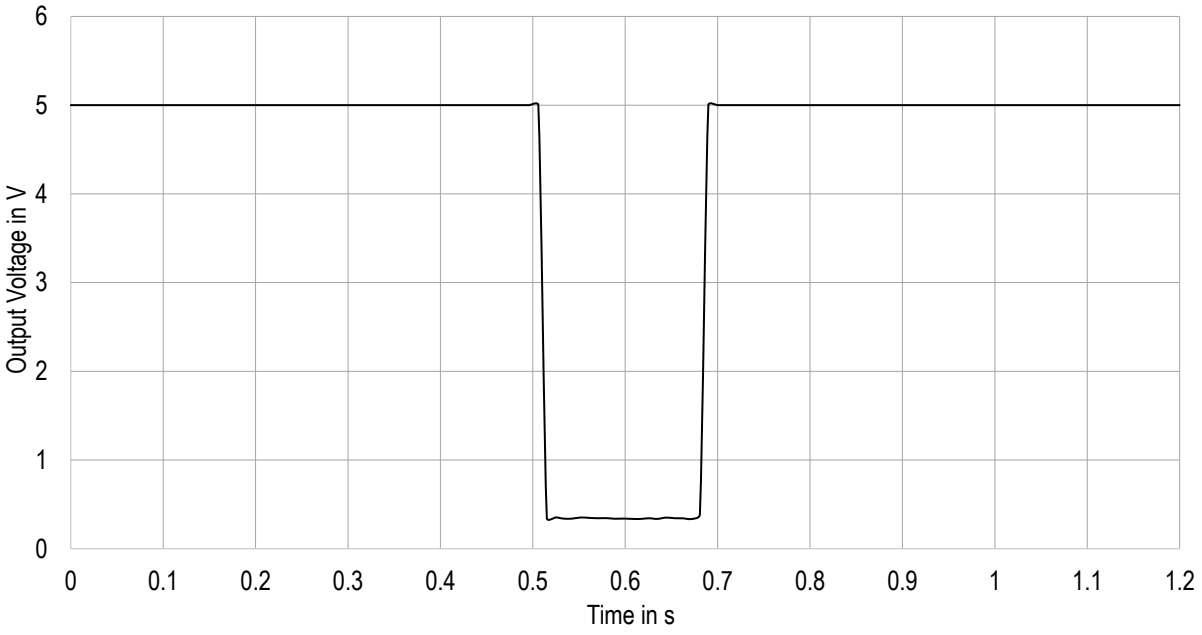


Figure 24: 173950575 short circuit protection, V_{IN} = 48V, V_{OUT} = 5V, I_{OUT} = 0.5A.

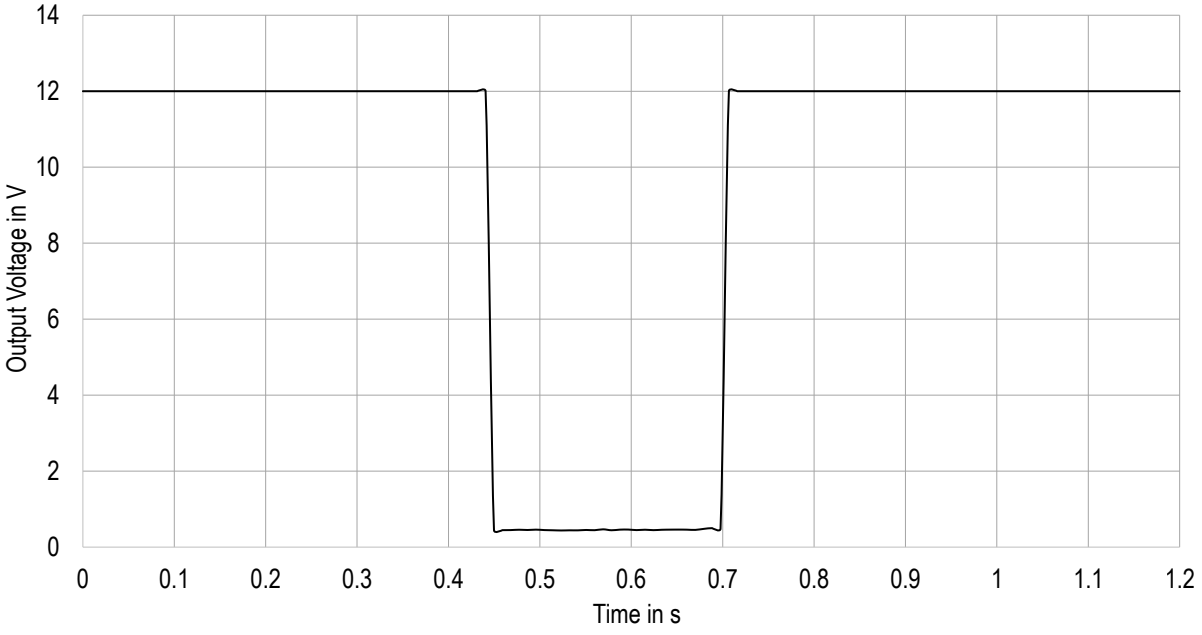


Figure 25: 173951275 short circuit protection, V_{IN} = 48V, V_{OUT} = 12V, I_{OUT} = 0.5A.

Over Current Protection (OCP)

For protection against load faults, the power module incorporates cycle by cycle current monitoring. During an over current condition the output current is limited and the output voltage drops. When the overcurrent condition is removed, the output voltage returns to the nominal voltage.

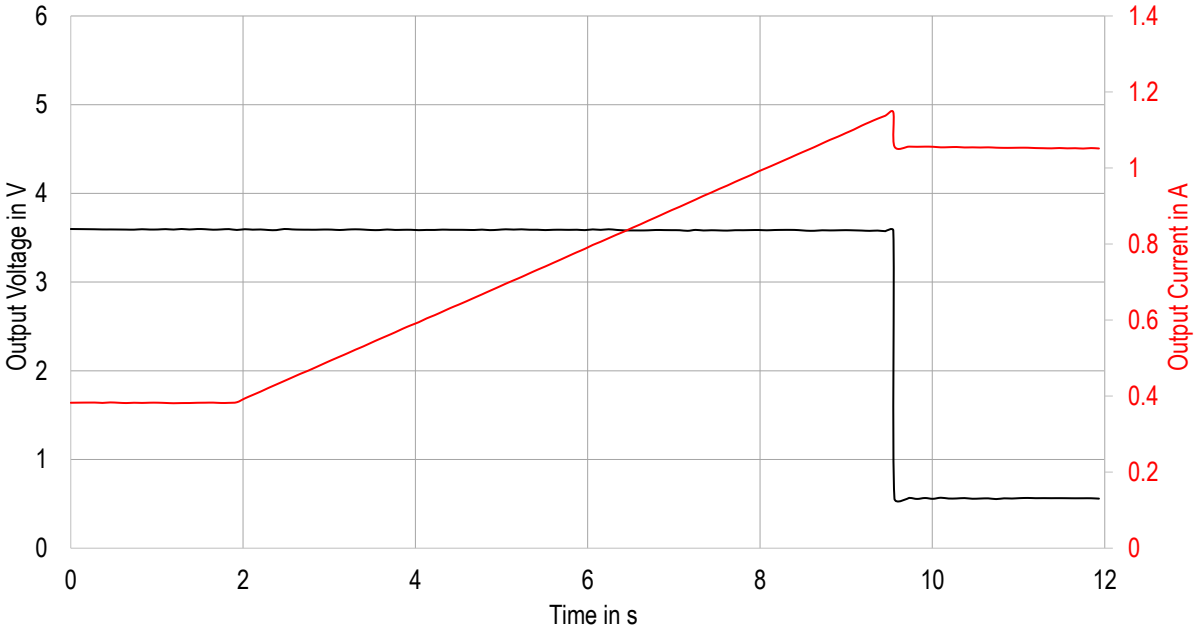


Figure 26: 173950375 over current protection, V_{IN} = 48V, V_{OUT} = 3.3V.

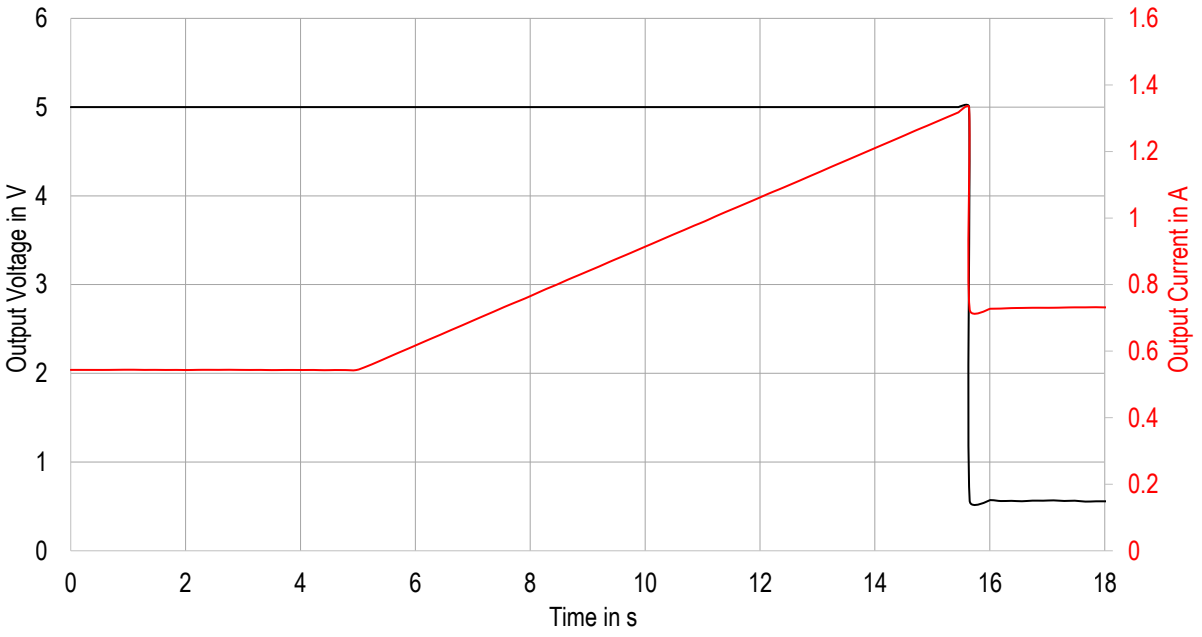


Figure 27: 173950575 over current protection, V_{IN} = 48V, V_{OUT} = 5V.

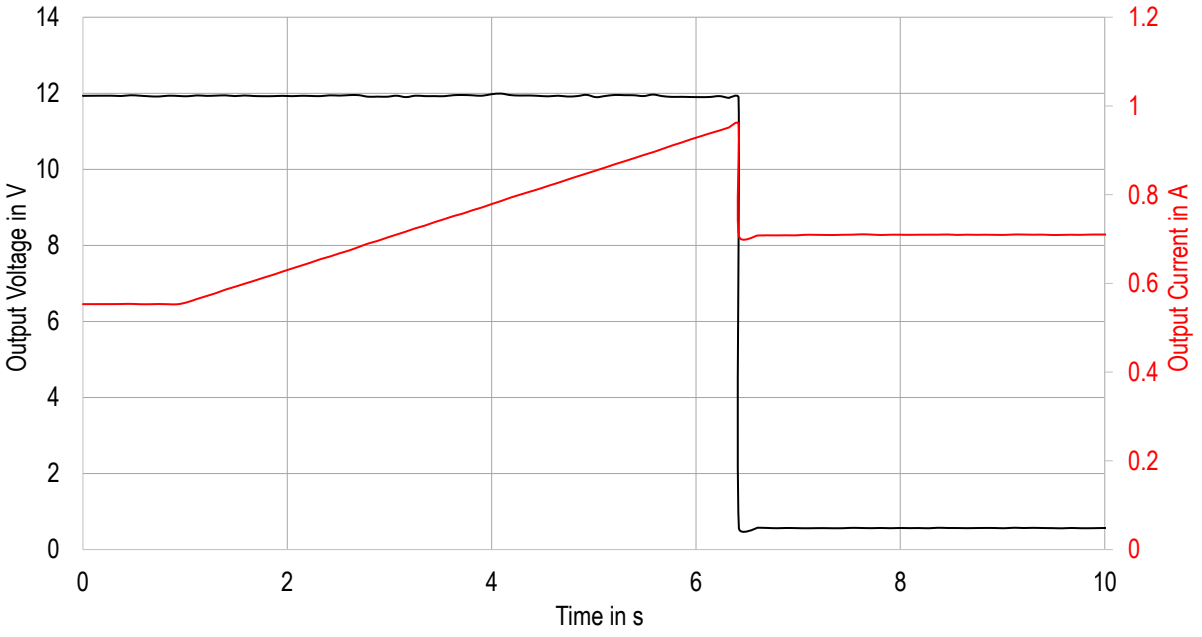


Figure 28: 173951275 over current protection, V_{IN} = 48V, V_{OUT} = 12V.

17 DESIGN EXAMPLE

The design example shows a possible solution for 48V to 3.3V (173950375), 5V (173950575) and 12V (173951275) with a max Iout of 0.5A. All of the necessary components to fullfill the requirements of the CISPR 32 EMI conducted- and radiated-emissions tests are included in the design example. It passes the conducted emissions class B with 0.8m input- and 1m output lines and passes the radiated emissions class B in a FAR at 3m measurement distance with 0.8m horizontal, 0.8m vertical input- and 1m horizontal outputlines. Filter components may be omitted depending on the requirements of the final application.

17.1 Layout

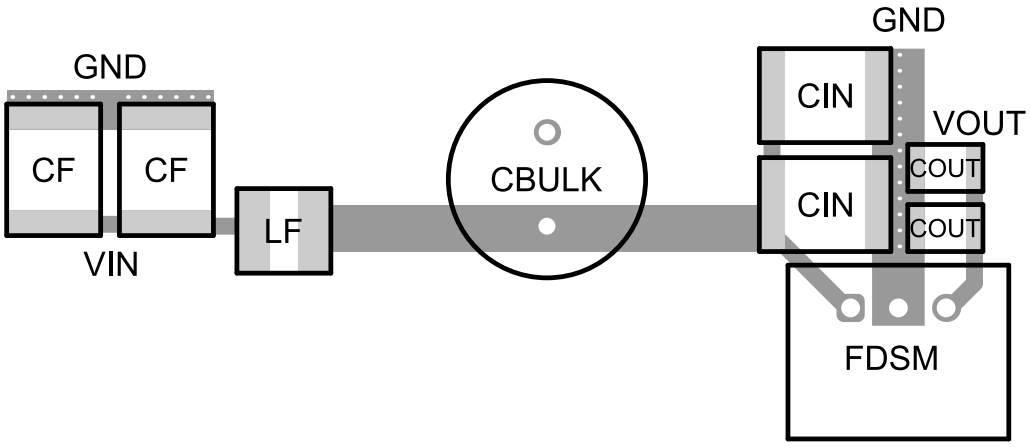


Figure 29: Layout recommendation.

The layout above has been evaluated to provide the optimal performance in terms of transient response, efficiency, ripple and EMI. The design footprint can be reduced at the expense of performance in these parameters.
The following recommendation should be followed when designing the layout:

1. The input and output capacitors should be placed as close as possible to the module pins.
2. The bulk capacitor should be placed close to the input capacitors of the module.
3. To avoid direct coupling of the DC/DC converter´s E- and H-fields into connectors, filter components and cables, the module must be placed as far away from these components as possible.

17.2 Schematic

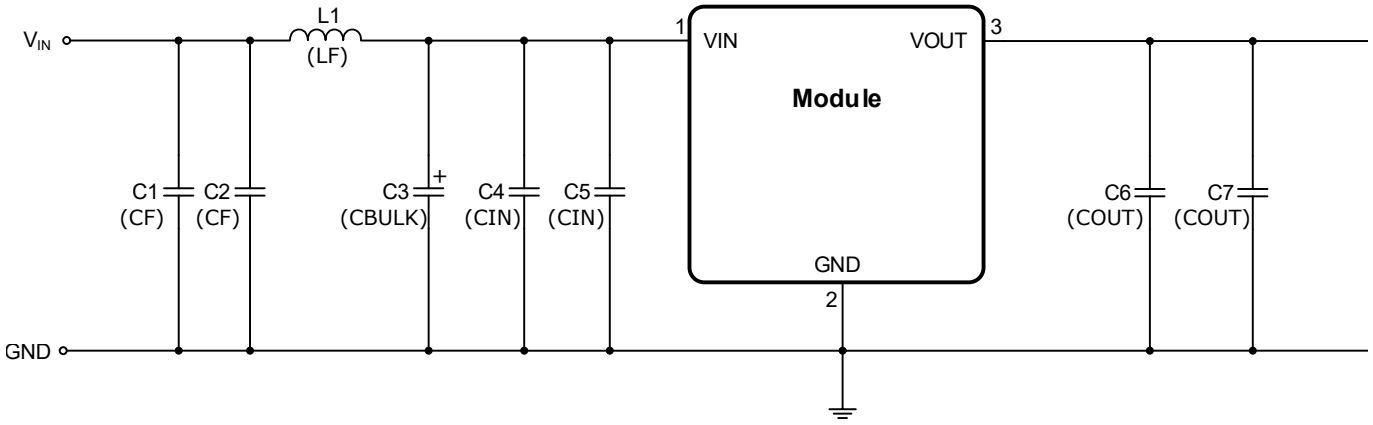


Figure 30: 17395xx75 design example schematic.

For optimal EMI performance, two filter capacitors, C1 and C2, one filter inductor, L1, two input capacitors, C4 and C5, and two output capacitors, C6 and C7 are recommended. For optimal electrical performance, two input capacitors, C4 and C5, and one output capacitor C6 are recommended. To damp possible input line oscillations the bulk capacitor, C3, is recommended.

17.3 Bill of Materials

Table 9: 17395xx75 design example bill of materials.

DESIGNATOR	DESCRIPTION	FUNCTION	QUANTITY	ORDER CODE	MANUFACTURER
U1	MagI³C Power Module	Power supply	1	17395xx75	WE
L1	Filter inductor, 4.7µH, PD2 family, I_{SAT} = 2.46A, I_R = 1.82A	Input Filter	1	744773047	WE
C1, C2	Ceramic chip capacitor 10µF/100V X7R, 2220	Input Filter	2	885012214006	WE
C3	Aluminum electrolytic capacitor 100µF/100V	Input Filter	1	860040875005	WE
C4, C5	Ceramic chip capacitor 10µF/100V X7R, 2220	Input Filter / Electrical Performance	2	885012214006	WE
C6, C7	Ceramic chip capacitor 22µF/25V X7R, 1210	Output Filter / Electrical Performance	2	885012209074	WE

GENERATING MULTIPLE OUTPUT VOLTAGES

A common requirement in industrial applications is to provide multiple supply voltages (e.g. 3.3V / 5V / 12V). The circuit below shows how this can be achieved by connecting the required members of the 17395xx75 family in parallel, all with the same source. The use of additional 2 times 22µF MLCCs is recommended to keep the output voltage ripple low. When using multiple modules connected to a single DC bus voltage it is recommended to decouple the individual module inputs by using LC filters. An LC filter at the input reduces the likelihood of oscillations due to parasitics of the layout as well as preventing the input ripple of the modules from affecting each other. The recommended EMI filter (see Recommended External Circuit for Best Performance) offers a good starting point for the decoupling filter design. Further evaluation of the filter and module within the application system in necessary to determine the best component values for optimal performance.

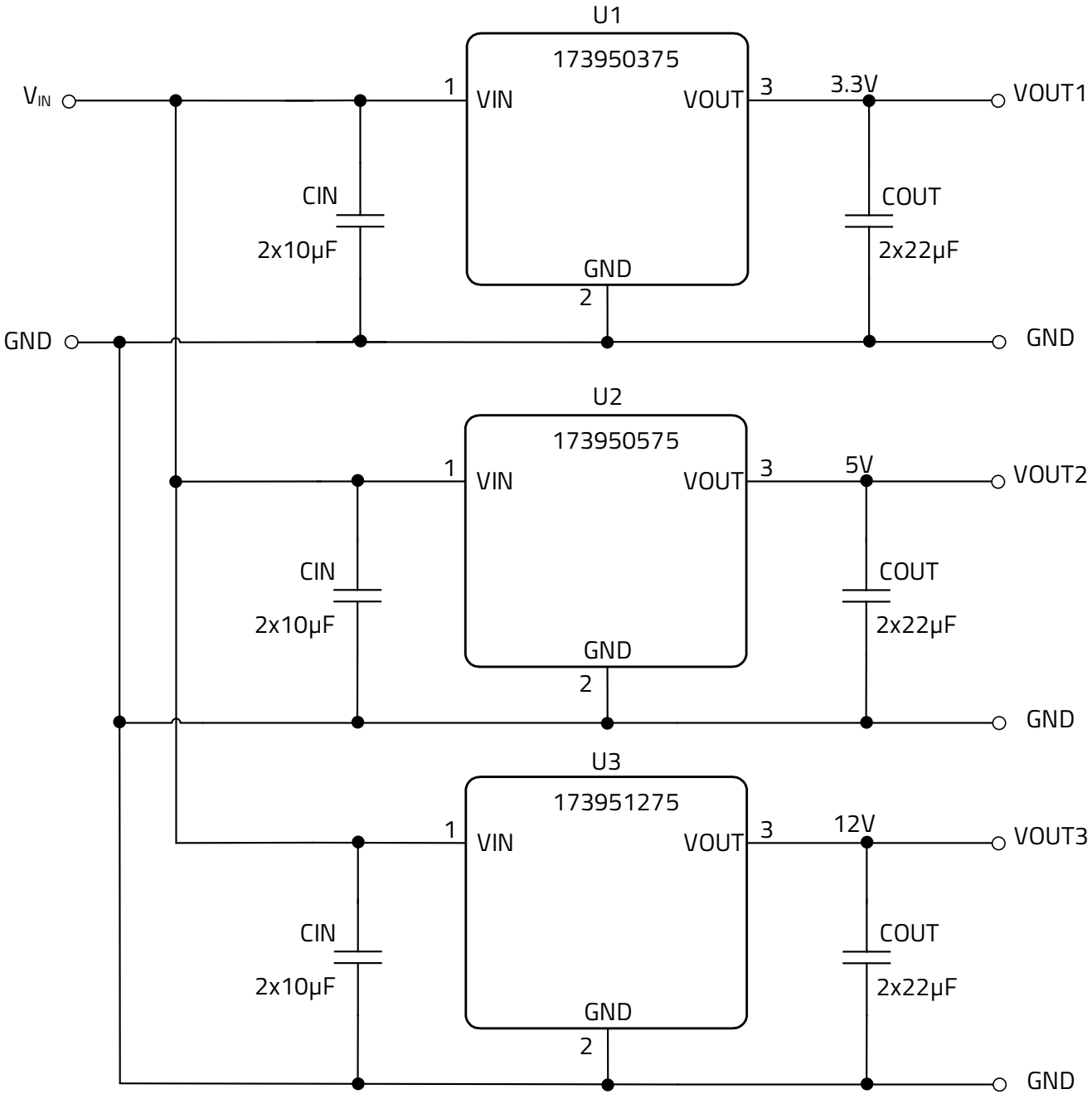


Figure 31: Multiple output voltage schematic.

18 HANDLING RECOMMENDATIONS

18.1 Solder Profile

Table 10: Wave solder profile.

Profile Feature	Old standard (Pb)	New (Pb-free)
Time within peak temperature t_p	10s	10s
Average ramp-up rate between T_s and T_p	200°C/s	200°C/s
Final preheat temperature Ts	130°C/s	130°C/s
Peak temperature T_p	+235°C/s	+260°C/s
Ramp-down rate	-5°C/s	-5°C/s
Heating rate during preheat	4°C/s	4°C/s

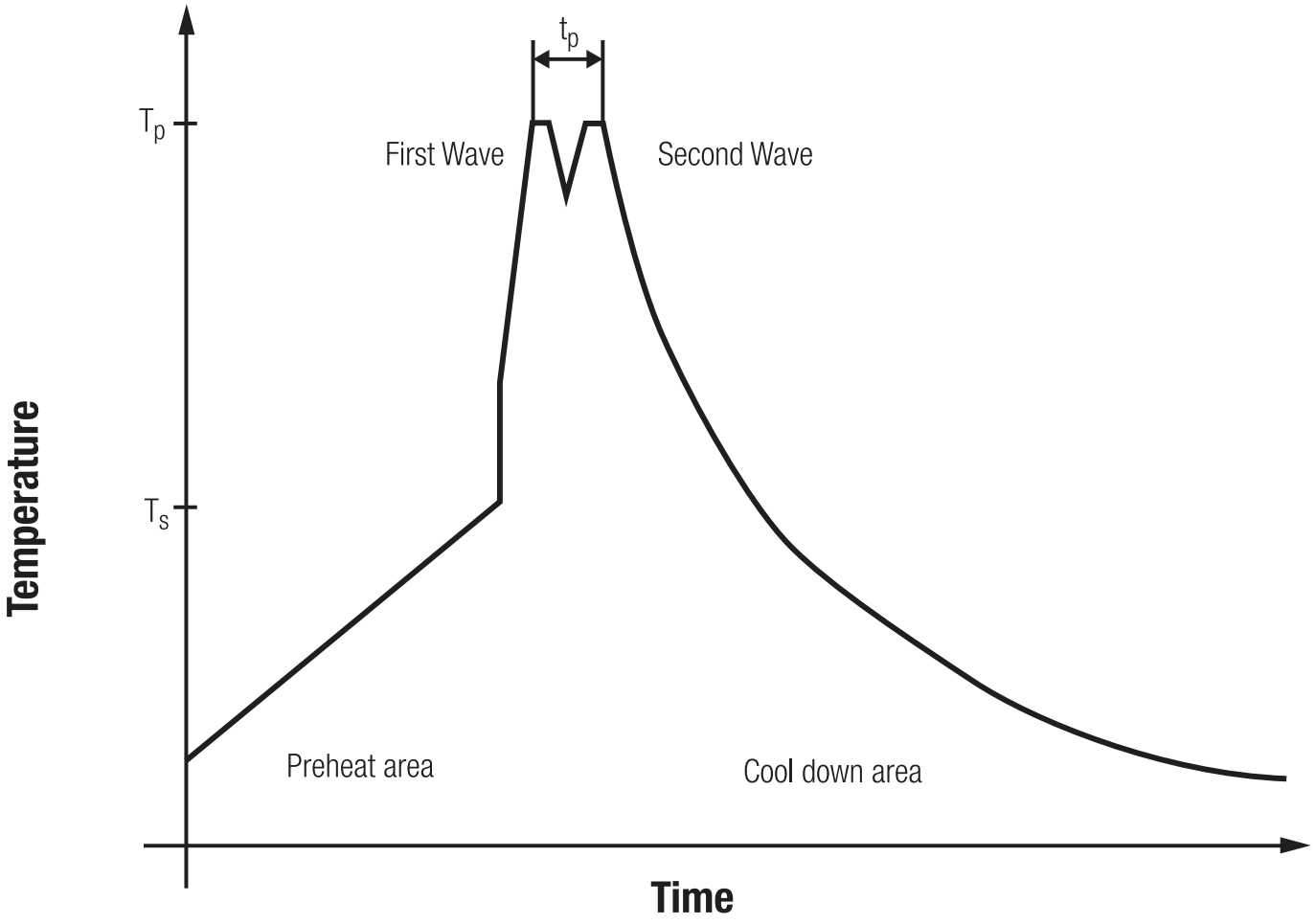


Figure 32: Solder profile.

19 PHYSICAL DIMENSIONS

19.1 Component

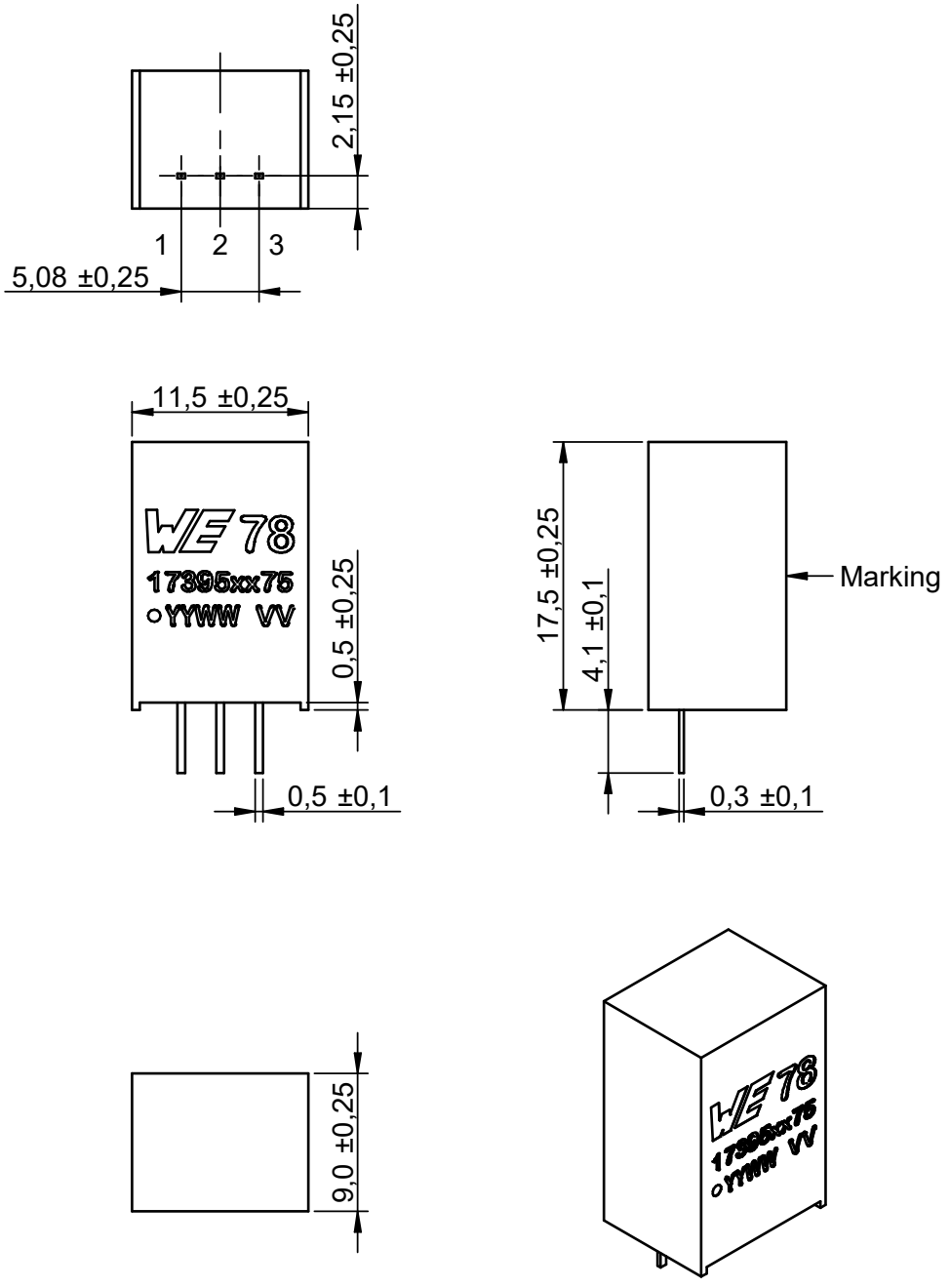


Figure 33: Physical dimensions.

19.2 Recommended Drill Holes

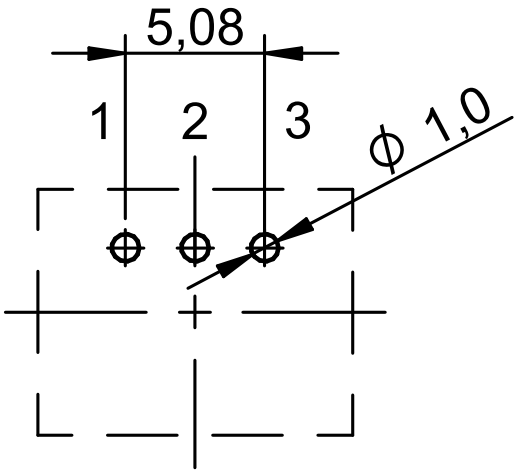


Figure 34: Recommended drill holes.

19.3 Tube

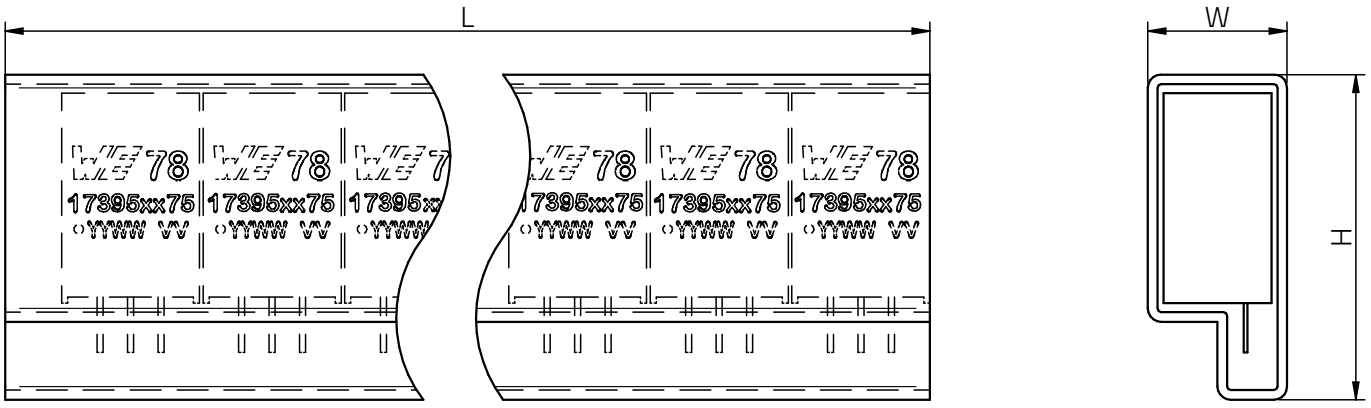


Figure 35: Tube.

Table 11: Packaging dimensions.

Dimension	L	W	H
Tolerance	typ.	typ.	typ.
Value	530mm	10mm	25.5mm

20 DOCUMENT HISTORY

Table 12: Document history.

Revision	Date	Description	Comment
1.0	November 2021	Initial data sheet release	
1.1	January 2022	Minor Change	Added pin position
1.2	July 2023	Minor Change	To improve the the EMI performance the EMI filter as well as the input capacitors recommendation was changed. For further noise damping the bulk capacitor CBULK has been added to the filter recommendation. Updated data sheet to the latest format.

21 LIST OF FIGURES

22 LIST OF TABLES

23 CAUTIONS AND WARNINGS

The following conditions apply to all goods within the product series of MagI³C of Würth Elektronik eiSos GmbH & Co. KG:

General:

- All recommendations according to the general technical specifications of the data-sheet have to be complied with.
- The usage and operation of the product within ambient conditions which probably alloy or harm the component surface has to be avoided.
- The responsibility for the applicability of customer specific products and use in a particular customer design is always within the authority of the customer. All technical specifications for standard products do also apply for customer specific products
- Residual washing varnish agent that is used during the production to clean the application might change the characteristics of the body, pins or termination. The washing varnish agent could have a negative effect on the long term function of the product. Direct mechanical impact to the product shall be prevented as the material of the body, pins or termination could flake or in the worst case it could break. As these devices are sensitive to electrostatic discharge customer shall follow proper IC Handling Procedures.
- Customer acknowledges and agrees that it is solely responsible for compliance with all legal, regulatory and safety-related requirements concerning its products, and any use of Würth Elektronik eiSos GmbH & Co. KG components in its applications, notwithstanding any applications-related information or support that may be provided by Würth Elektronik eiSos GmbH & Co. KG.
- Customer represents and agrees that it has all the necessary expertise to create and implement safeguards which anticipate dangerous consequences of failures, monitor failures and their consequences lessen the likelihood of failures that might cause harm and take appropriate remedial actions
- Customer will fully indemnify Würth Elektronik eiSos and its representatives against any damages arising out of the use of any Würth Elektronik eiSos GmbH & Co. KG components in safety-critical applications

Product specific:

Follow all instructions mentioned in the datasheet, especially:

- The solder profile has to comply with the technical reflow or wave soldering specification, otherwise this will void the warranty.
- All products are supposed to be used before the end of the period of 12 months based on the product date-code.
- Violation of the technical product specifications such as exceeding the absolute maximum ratings will void the warranty.
- It is also recommended to return the body to the original moisture proof bag and reseal the moisture proof bag again.
- ESD prevention methods need to be followed for manual handling and processing by machinery.

Disclaimer:

This electronic component has been designed and developed for usage in general electronic equipment only. This product is not authorized for use in equipment where a higher safety standard and reliability standard is especially required or where a failure of the product is reasonably expected to cause severe personal injury or death, unless the parties have executed an agreement specifically governing such use. Moreover Würth Elektronik eiSos GmbH & Co. KG products are neither designed nor intended for use in areas such as military, aerospace, aviation, nuclear control, submarine, transportation (automotive control, train control, ship control), transportation signal, disaster prevention, medical, public information network etc. Würth Elektronik eiSos GmbH & Co. KG must be informed about the intent of such usage before the design-in stage. In addition, sufficient reliability evaluation checks for safety must be performed on every electronic component which is used in electrical circuits that require high safety and reliability functions or performance. These cautions and warnings comply with the state of the scientific and technical knowledge and are believed to be accurate and reliable. However, no responsibility is assumed for inaccuracies or incompleteness.

24 IMPORTANT NOTES

General Customer Responsibility

Some goods within the product range of Würth Elektronik eiSos GmbH & Co. KG contain statements regarding general suitability for certain application areas. These statements about suitability are based on our knowledge and experience of typical requirements concerning the areas, serve as general guidance and cannot be estimated as binding statements about the suitability for a customer application. The responsibility for the applicability and use in a particular customer design is always solely within the authority of the customer. Due to this fact it is up to the customer to evaluate, where appropriate to investigate and decide whether the device with the specific product characteristics described in the product specification is valid and suitable for the respective customer application or not. Accordingly, the customer is cautioned to verify that the datasheet is current before placing orders.

Customer Responsibility Related to Specific, in Particular Safety-Relevant, Applications

It has to be clearly pointed out that the possibility of a malfunction of electronic components or failure before the end of the usual lifetime cannot be completely eliminated in the current state of the art, even if the products are operated within the range of the specifications. In certain customer applications requiring a very high level of safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health it must be ensured by most advanced technological aid of suitable design of the customer application that no injury or damage is caused to third parties in the event of malfunction or failure of an electronic component.

Best Care and Attention

Any product-specific notes, warnings and cautions must be strictly observed. Any disregard will result in the loss of warranty.

Customer Support for Product Specifications

Some products within the product range may contain substances which are subject to restrictions in certain jurisdictions in order to serve specific technical requirements. Necessary information is available on request. In this case the field sales engineer or the internal sales person in charge should be contacted who will be happy to support in this matter.

Product R&D

Due to constant product improvement product specifications may change from time to time. As a standard reporting procedure of the Product Change Notification (PCN) according to the JEDEC-Standard we inform about minor and major changes. In case of further queries regarding the PCN, the field sales engineer or the internal sales person in charge should be contacted. The basic responsibility of the customer as per Section 1 and 2 remains unaffected.

Product Life Cycle

Due to technical progress and economical evaluation we also reserve the right to discontinue production and delivery of products. As a standard reporting procedure of the Product Termination Notification (PTN) according to the JEDEC Standard we will inform at an early stage about inevitable product discontinuance. According to this we cannot guarantee that all products within our product range will always be available. Therefore it needs to be verified with the field sales engineer or the internal sales person in charge about the current product availability expectancy before or when the product for application design-in disposal is considered. The approach named above does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.

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