WURTH ELEKTRONIK MORE THAN YOU EXPECT





QUICK START GUIDE

Magl³C Power Module Evaluation Board for 171936001 LGA-12

Evaluation Board 178936001

Version 1.0

SCHEMATIC

Features



















FN55032 Class B

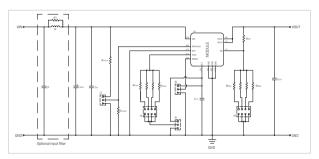
FIX SS ADJ. Freg.

Svnc.

UVLO

UCD

T. 105°C



The additional aluminum polymer capacitor C_{BULK} is only for evaluation board protection purposes. It is mounted at the termination. of the supply line and provides slight damping of possible oscillations of the series resonance circuit represented by the inductance of the supply line and the input capacitance. It is not essential for operation.

For accurate V_{IN} and V_{OUT} voltage measurements it is recommended to measure directly at the test pins placed beside the input and output capacitors CIN and COLIT. It is not recommended to use this evaluation hoard with input and output wire lengths longer than 1 m

To optimize the EMI performance connect the R_{FSW} resistor to VCC to enable spead spectrum behavior.

For the datasheet of the power module visit us at: https://www.we-online.com/ catalog/en/MAGIC-VDMM



This product is highly sensitive to electrostatic discharge (ESD). As such, always use proper ESD precautions when handling. Failing to follow the aforementioned recommendations can result in severe damage to the part.

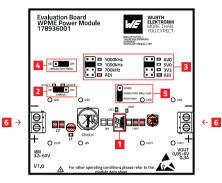


Ref. Des.	Description Order Code	
U1	Magl³C Power Module (171936001)	
C _{BULK}	Aluminum Polymer Capacitor 12 µF / 100 V (875115957002)	
C _{IN}	2 x Ceramic Chip Capacitor 1µF/100V/X7R, 1210 (885012209069)	
\mathbf{C}_{vcc}	Ceramic Chip Capacitor 470 nF / 16 V / X7R, 0805 (885012207049)	
C _{OUT}	Ceramic Chip Capacitor 4.7 µF/16V/X7R, 1210 (885012209013) + 2 x Ceramic Chip Capacitor 4.7 µF/16V/X7R, 1210 (885012209013) (optional) + Aluminum Polymer Capacitor 15 uF/16V, V-Chip (875105359002) (optional)	
Cf	2 x Ceramic Chip Capacitor 1 µF / 100 V / X7R, 1210 (optional) (885012209069)	
Lf	Filter inductor, 2.2 µH, PD2 (optional) (744773022)	
R _{FBT}	510 kΩ	
R _{FBB}	Set by jumper	84.5 kΩ for V _{OUT} = 6 V
		105 kΩ for V_{OUT} = 5 V
		178 kΩ for V _{OUT} = 3.3 V
		To be soldered for adjustable output voltage $V_{OUT} = V_{REF} \left(\frac{R_{FBT}}{R_{FBB}} + 1 \right)$
R _{FSW}	Set by jumper	0 Ω for FSW = 1000 kHz for 5 V _{OUT} & 6 V _{OUT}
		10 kΩ for FSW = 700 kHz for $3.3 V_{OUT}$
		To be soldered for adjustable frequency
J1	Jumper for EN connection to either $V_{\scriptscriptstyle N}$ (device enabled) or GND (device disabled) (61300311121)	
J2	Jumper for switching frequency selection. Only one resistor should be selected at a time (61300821121)	
J3	Jumper for output voltage selection. Only one resistor should be selected at a time (61300821121)	
J4	Jumper to enable & disable spread spectrum behavior (61300311121)	
J5	Jumper for MODE connection to either VCC(FPWM) or GND(PFM/PWM)(61300311121)	





OVERVIEW



Absolute maximum ratings

Caution: Exceeding the abs. max. values given in the datasheet may affect the device negatively and may cause permanent damage.

This evaluation board is intended to be operated in a research and development environment under the supervision of qualified technicians and engineers who are trained and experienced in the safe use of electronics. This evaluation board was designed and tested according to CISPR32 Class B standards under Würth Elektronik laboratory test conditions, as indicated in the data sheet of

Description

V_{IN} 3.5 – 60 V V_{OUT} 0.85 V to 6 V

Inux 0.3 V

- VDMM Variable Step Down MicroModule
- Jumpers (J1) for ENABLE & shut off the Module
- Jumpers to set predefined output voltage Vour and fsw
- Jumpers to enable & disable spread spectrum behavior
- Jumpers to change between FPWM and PFM/PWM. The Mode/Sync can be used to synchronise the power module to an external clock
- Terminal block screw connectors for V_{IN} and V_{OLIT}

the corresponding power module. Operation in other test setups may cause unintended electrical behavior and exceed the stated performance and limits imposed by the CISPR32 Class B standards. This evaluation board is not intended for usage in final applications. This evaluation board is not intended for resale.

