



Product / Process Change Notification (PCN)

- Major Change
 Minor Change

PCN Number: PCN_WPME-VDMM_20241016 Affected Series: WPME-VDMM Affected Order Codes: 171960501, 171010501, 171010502, 171010550 PCN Date: 2024-07-16 (YYYY-MM-DD) Effective Date: 2024-10-16 (YYYY-MM-DD)	Change Category: <input type="checkbox"/> Equipment/Location <input checked="" type="checkbox"/> General Data <input type="checkbox"/> Material <input type="checkbox"/> Process <input type="checkbox"/> Product Design <input type="checkbox"/> Shipping/Packaging <input type="checkbox"/> Supplier <input type="checkbox"/> Software
Contact: Product Management Phone: +49 (0) 7942 - 945 5001 Fax: +49 (0) 7942 - 945 5179 E-Mail: pcn.eisos@we-online.com	Datasheet Change: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No Attachment: <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No

Description of Change:
 For the purpose of a datasheet information enlargement, Würth Elektronik eiSos has updated the electrical specifications, the design flow and the handling recommendations. The updated datasheets are attached to this PCN.
 This is a datasheet correction only. There will be no change in form, fit, function, quality or reliability of the products.

Details of Change:
 Changes 1 through 5 apply to all order codes indicated in this PCN.

1. Line and load regulation units have been changed

171960501										
Before Change					After Change					
Line regulation	$V_{IN} = 2.7V$ to $5V$, $T_A = 25^\circ C$, MODE = high	-	± 0.2	-	%V	Line regulation	$V_{IN} = 2.7V$ to $5.5V$, MODE = high	—	0.2	%
Load regulation	$V_{IN} = 2.7V$, $V_{OUT} = 1.8V$ over Iour range, MODE = high, $T_A = 25^\circ C$	-	± 0.5	-	%A	Load regulation	$0A \leq I_{LOAD} \leq 0.6A$, MODE = high	—	0.5	%



171010501										
Before Change					After Change					
Line regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, MODE = low	-	0.05	-	%/V	Line regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, MODE = low	—	0.05	%
Load regulation	$500mA \leq I_{LOAD} \leq 1A$	-	-0.9	-	%/A	Load regulation	$500mA \leq I_{LOAD} \leq 1A$	—	0.9	%

171010502										
Before Change					After Change					
Line regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, MODE = low	-	0.05	-	%/V	Line regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, MODE = low	—	0.05	%
Load regulation	$500mA \leq I_{LOAD} \leq 1A$	-	-0.9	-	%/A	Load regulation	$500mA \leq I_{LOAD} \leq 1A$	—	0.9	%

171010550											
Before Change					After Change						
Line regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, MODE = low	-	0.04	0.2	%/V	Line regulation	$V_{IN} = V_{OUT} + 1V$ to 5.5V, MODE = low	—	0.04	0.2	%
Load regulation	$500mA \leq I_{LOAD} \leq 1A$	-	-0.9	-	%/A	Load regulation	$500mA \leq I_{LOAD} \leq 1A$	—	-0.9	—	%

2. Input current values have been corrected

171960501													
Before Change					After Change								
Input quiescent/shutdown current					Input Quiescent and Shutdown Current								
I_{SD}	Shutdown current	$V_{ENABLE} = 0V, T_A = 25^\circ C$	-	0.1	1	μA	I_{SD}	Shutdown current	$V_{EN} = 0V$	—	0.1	1	μA
I_{IN}	No load input current	MODE = high, EN = high, switching with no load, $V_{OUT} = 1.8V, T_A = 25^\circ C$	-	6	-	mA	I_{IN}	No load input current	Mode = high, Enable = high, switching with no load	—	6	—	mA
		MODE = low, EN = high, switching with no load, $V_{OUT} = 1.8V, T_A = 25^\circ C$	-	3	-	mA			Mode = low, Enable = high, switching with no load	—	80	—	μA
I_Q	Quiescent current	MODE = low, EN = high no switching, $T_A = 25^\circ C$	-	30	-	μA	I_Q	Quiescent current	Mode = low, Enable = high, no switching	—	60	—	μA

171010501													
Before Change					After Change								
Input quiescent/shutdown current					Input Quiescent and Shutdown Current								
I_{SD}	Shutdown current	$V_{ENABLE} = 0V, T_A = 25^\circ C$	-	0.1	1	μA	I_{SD}	Shutdown current	ENABLE = low	—	0.1	—	μA
I_{IN}	No load input current	MODE = high, EN = high, switching with no load, $V_{OUT} = 1.8V, T_A = 25^\circ C$	-	6	-	mA	I_{IN}	No load input current	$V_{OUT} = 3.3V, ENABLE = high, switching, no load$	—	40	—	μA
		MODE = low, EN = high, switching with no load, $V_{OUT} = 1.8V, T_A = 25^\circ C$	-	3	-	mA			ENABLE = high, switching, no load	—	25	—	μA
I_Q	Quiescent current	MODE = low, EN = high no switching, $T_A = 25^\circ C$	-	30	-	μA	I_Q	Quiescent current	ENABLE = high, no switching	—	22	—	μA

171010502													
Before Change					After Change								
Input Quiescent and Shutdown Current					Input Quiescent and Shutdown Current								
I_{SD}	Shutdown current	$V_{EN} = low, V_{IN} = 5V$	-	0.5	-	μA	I_{SD}	Shutdown current	$V_{EN} = low, V_{IN} = 5V$	—	0.1	—	μA
I_{IN}	No load input current	MODE = high, Enable = high, switching with no load, $V_{OUT} = 1.8V$	-	13	-	mA	I_{IN}	No load input current	MODE = high, switching with no load	—	12.8	—	mA
		MODE = low, Enable = high, switching with no load, $V_{OUT} = 1.8V$	-	25	-	μA			MODE = low, switching with no load	—	27	—	μA
I_Q	Quiescent current	MODE = high, Enable = high, no switching, $V_{OUT} = 1.8V$	-	25	-	μA	I_Q	Quiescent current	MODE = high, no switching	—	23.5	—	μA
		MODE = low, Enable = high, no switching, $V_{OUT} = 1.8V$	-	25	-	μA			MODE = low, no switching	—	22.5	—	μA



171010550

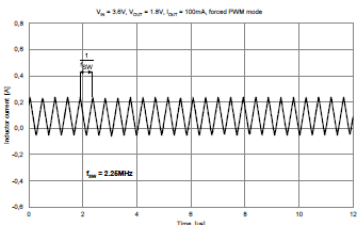
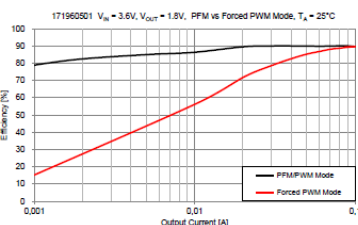
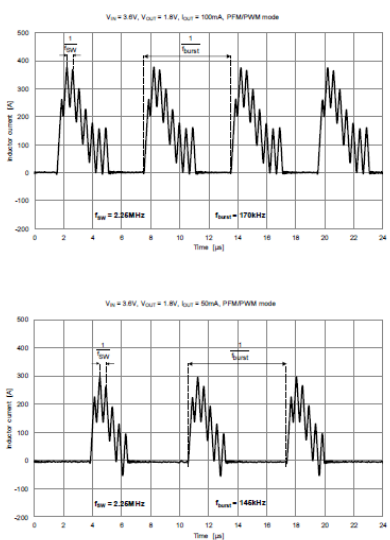
Before Change

Input Quiescent and Shutdown Current						
I _{SD}	Shutdown current	V _{IN} = low, V _{IN} = 5V	-	0.1	-	μA
		V _{IN} = low, V _{IN} = 2.5V	-	0.09	-	μA
I _{IN}	No load input current	MODE = high, Enable = high, switching with no load	-	13	-	mA
		MODE = high, Enable = high, switching with no load	-	25	-	μA
I _Q	Quiescent current	MODE = high, Enable = high, no switching, V _{IN} = 2.5V	-	25	-	μA
		MODE = high, Enable = high, no switching	-	25	-	μA

After Change

Input Quiescent and Shutdown Current						
I _{SD}	Shutdown current	ENABLE = low	-	0.1	-	μA
		V _{OUT} = 3.3V, MODE = low, switching, no load	-	40	-	μA
I _{IN}	No load input current	V _{OUT} = 3.3V, MODE = high, switching, no load	-	12.7	-	mA
		V _{IN} = 3.3V, MODE = low, switching, no load	-	27.7	-	μA
		V _{IN} = 3.3V, MODE = high, switching, no load	-	9.6	-	mA
		MODE = low, ENABLE = high, no switching	-	22	-	μA
I _Q	Quiescent current	MODE = high, ENABLE = high, no switching	-	250	-	μA

3. Modes of operation page rewritten

171960501	
Before Change	After Change
<p>LIGHT LOAD OPERATION</p> <p>Under light load conditions the 171960501 behaves differently according to the MODE pin setting.</p> <p>Forced PWM mode</p> <p>Pulling the MODE pin high selects the forced PWM mode. In this mode the device constantly functions in PWM mode, switching at the default 2.25 MHz fixed switching frequency, independently of the load (see figure below). This mode of operation eases the filtering requirements to help with noise sensitive applications.</p>  <p style="font-size: small; text-align: center;">V_{IN} = 3.6V, V_{OUT} = 1.8V, I_{OUT} = 100mA, forced PWM mode</p> <p style="text-align: center;">f_{SW} = 2.25MHz</p> <p>PFM/PWM mode</p> <p>Setting the MODE pin low selects the PFM/PWM mode. This mode achieves a much higher efficiency at light loads (normally below 100mA), as shown in the picture below.</p>  <p style="font-size: small; text-align: center;">171960501 V_{IN} = 3.6V, V_{OUT} = 1.8V, PFM vs Forced PWM Mode, T_A = 25°C</p> <p>In PFM/PWM mode the energy is delivered in bursts to the load (see figure below). Within each burst the device switches at the default switching frequency and the energy is delivered to both the load and the output capacitor. Between two bursts, the device does not switch (the load demand is supported by the output capacitor) and the current consumption is significantly reduced, leading to higher efficiency compared to the forced PWM mode. The frequency of the bursts (bursts) depends on the load and it is much lower than the default switching frequency (see picture below at two different load conditions). When the load current is above 100mA, the transition from the PFM mode to the PWM mode takes place automatically.</p>  <p style="font-size: small; text-align: center;">V_{IN} = 3.6V, V_{OUT} = 1.8V, I_{OUT} = 100mA, PFM/PWM mode</p> <p style="font-size: small; text-align: center;">V_{IN} = 3.6V, V_{OUT} = 1.8V, I_{OUT} = 50mA, PFM/PWM mode</p> <p style="font-size: x-small;">Please consider that the burst frequency f_{burst} can differ from the example measurements above depending on several parameters (e.g. C_{OUT}, C_{ES}).</p>	<p>15 MODES OF OPERATION</p> <p>The MODE pin of the 171960501 can be pulled either high or low to alter the light load performance of the power module based on the application requirements. When pulled high, PWM operation will be forced throughout the entire load current range. When pulled low, PFM operation will occur during light load conditions. The power module will operate in one of three modes, depending on the operating conditions.</p> <p>15.1 Pulse Width Modulation (PWM) Operation</p> <p>The power module operates at a fixed switching frequency of 2.25MHz where the duty cycle (D) is determined by the following equation:</p> $D = \frac{V_{OUT}}{V_{IN}} \quad (4)$ <p>The on-time is determined by the duty cycle and the switching frequency as follows:</p> $t_{ON} = \frac{D}{f_{SW}} \quad (5)$ <p>The on and off-times can be related to the switching frequency as follows:</p> $\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$ <p>15.2 100% Duty Cycle Operation</p> <p>When the input voltage approaches the output voltage and the duty cycle approaches 100%, the power module will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.</p> <p>15.3 Pulse Frequency Modulation (PFM) Operation</p> <p>If the MODE pin is pulled low PFM operation is initiated when the power module enters discontinuous mode. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency. The frequency of the bursts depends on the load and is significantly lower than the default switching frequency. As the output current demand increase the bursts become more frequent until the module automatically transitions out of PFM operation. The burst frequency is a function of input voltage, output voltage, output current, C_{rr}, and C_{OUT}. Changing any of these parameters will alter the device's behavior during PFM operation.</p>



171010501	
Before Change	After Change
<p>MODES OF OPERATION</p> <p>The MicroModule will operate in one of four modes, depending on the operating conditions.</p> <p>Constant on-time (COT) operation</p> <p>The MicroModule operates at a fixed switching frequency of 4MHz where the duty cycle (DC) is determined by the following equation:</p> $DC = \frac{V_{OUT}}{V_{IN}} \quad (4)$ <p>The on-time is determined by the duty cycle and the switching frequency as follows:</p> $t_{ON} = \frac{DC}{f_{SW}} \quad (5)$ <p>The on and off-times can be related to the switching frequency as follows:</p> $\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$ <p>The mode of operation has a minimum off-time value of 60ns.</p> <p>Fixed off-time operation</p> <p>When the minimum off-time of 60ns is reached and the duty cycle must increase further, the MicroModule fixes the off-time to 60ns and begins increasing the on-time. This results in a decrease in switching frequency proportional to the increase in duty cycle.</p> <p>100% duty cycle operation</p> <p>When the input voltage approaches the output voltage and the duty cycle approaches 100%, the MicroModule will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.</p> <p>Power save operation</p> <p>Power save operation is initiated when the MicroModule enters discontinuous mode, typically occurring between 0mA and 300mA. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency.</p>	<p>15 MODES OF OPERATION</p> <p>The power module will operate in one of four modes, depending on the operating conditions.</p> <p>15.1 Constant On-Time (COT) Operation</p> <p>The power module operates at a fixed switching frequency of 4MHz where the duty cycle (D) is determined by the following equation:</p> $D = \frac{V_{OUT}}{V_{IN}} \quad (4)$ <p>The on-time is determined by the duty cycle and the switching frequency as follows:</p> $t_{ON} = \frac{D}{f_{SW}} \quad (5)$ <p>The on and off-times can be related to the switching frequency as follows:</p> $\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$ <p>The mode of operation has a minimum off-time value of 60ns.</p> <p>15.2 Fixed Off-Time Operation</p> <p>When the minimum off-time of 60ns is reached and the duty cycle must increase further, the power module fixes the off-time to 60ns and begins increasing the on-time. This results in a decrease in switching frequency proportional to the increase in duty cycle.</p> <p>15.3 100% Duty Cycle Operation</p> <p>When the input voltage approaches the output voltage and the duty cycle approaches 100%, the power module will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.</p> <p>15.4 Pulse Frequency Modulation (PFM) Operation</p> <p>PFM operation is initiated when the power module enters discontinuous mode. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency.</p>



171010502

Before Change

After Change

MODES OF OPERATION

The MODE pin of the 171010502 can be pulled either high or low to alter the light load performance of the module based on the application requirements.
 When pulled high, COT operation will be forced throughout the entire load current range. When pulled low, power save operation will occur during light load conditions.
 The MicroModule will operate in one of four modes, depending on the operating conditions.

Constant on-time (COT) operation

The MicroModule operates at a fixed switching frequency of 4MHz where the duty cycle (DC) is determined by the following equation:

$$DC = \frac{V_{OUT}}{V_{IN}} \quad (4)$$

The on-time is determined by the duty cycle and the switching frequency as follows:

$$t_{ON} = \frac{DC}{f_{SW}} \quad (5)$$

The on and off-times can be related to the switching frequency as follows:

$$\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$$

The mode of operation has a minimum off-time value of 60ns.

Fixed off-time operation

When the minimum off-time of 60ns is reached and the duty cycle must increase further, the MicroModule fixes the off-time to 60ns and begins increasing the on-time. This results in a decrease in switching frequency proportional to the increase in duty cycle.

100% duty cycle operation

When the input voltage approaches the output voltage and the duty cycle approaches 100%, the MicroModule will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.

Power save operation

Power save operation is initiated when the MicroModule enters discontinuous mode, typically occurring between 0mA and 300mA. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency.

15 MODES OF OPERATION

The MODE pin of the 171010502 can be pulled either high or low to alter the light load performance of the power module based on the application requirements.
 When pulled high, COT operation will be forced throughout the entire load current range. When pulled low, PFM operation will occur during light load conditions.
 The power module will operate in one of four modes, depending on the operating conditions.

15.1 Constant On-Time (COT) Operation

The power module operates at a fixed switching frequency of 4MHz where the duty cycle (D) is determined by the following equation:

$$D = \frac{V_{OUT}}{V_{IN}} \quad (4)$$

The on-time is determined by the duty cycle and the switching frequency as follows:

$$t_{ON} = \frac{D}{f_{SW}} \quad (5)$$

The on and off-times can be related to the switching frequency as follows:

$$\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$$

The mode of operation has a minimum off-time value of 60ns.

15.2 Fixed Off-Time Operation

When the minimum off-time of 60ns is reached and the duty cycle must increase further, the power module fixes the off-time to 60ns and begins increasing the on-time. This results in a decrease in switching frequency proportional to the increase in duty cycle.

15.3 100% Duty Cycle Operation

When the input voltage approaches the output voltage and the duty cycle approaches 100%, the power module will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.

15.4 Pulse Frequency Modulation (PFM) Operation

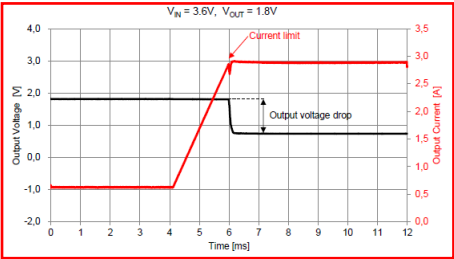
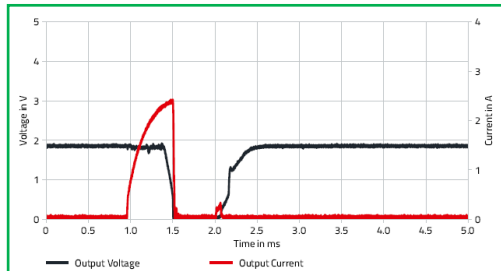
If the MODE pin is pulled low PFM operation is initiated when the power module enters discontinuous mode. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency.

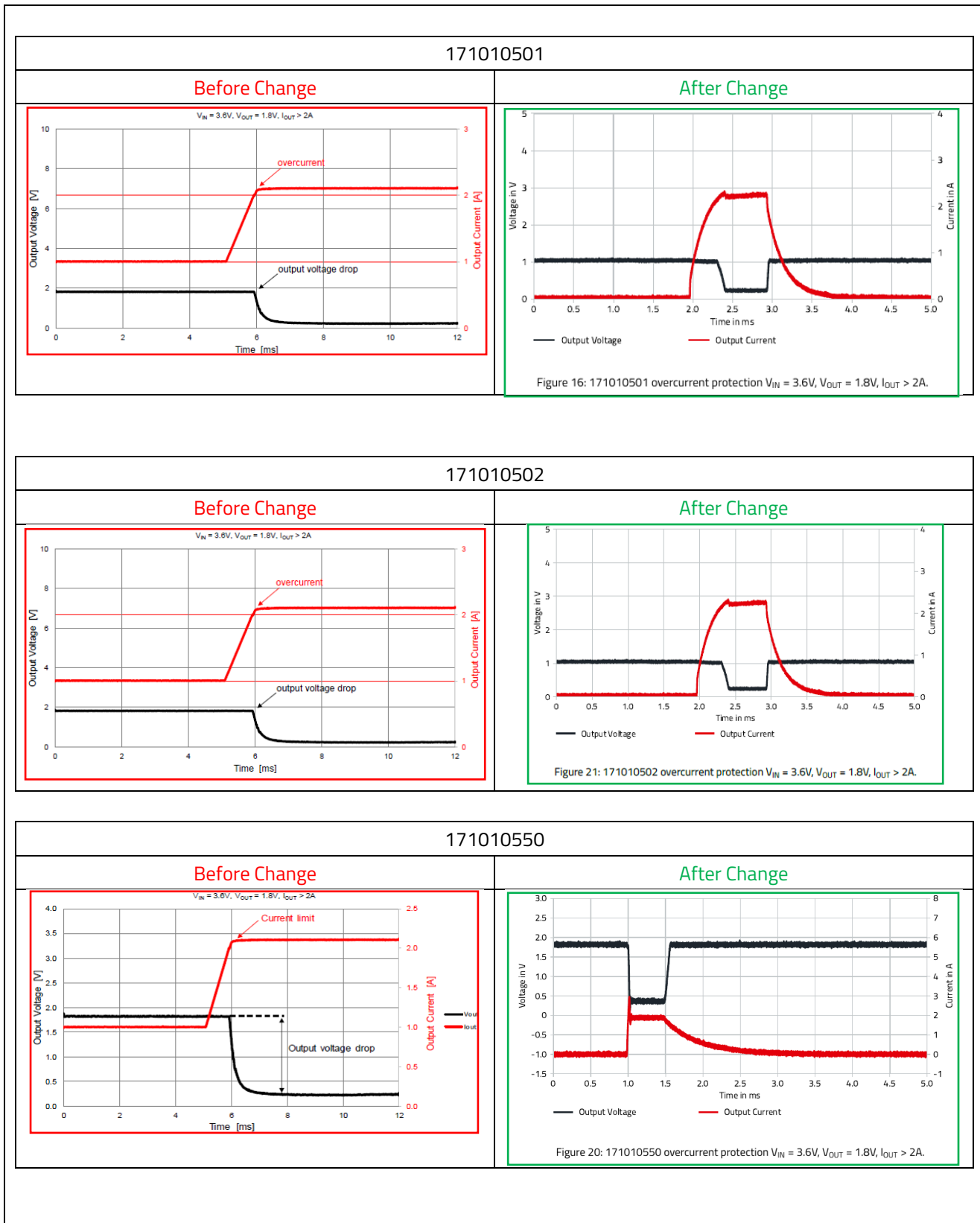
The frequency of the bursts depends on the load and is significantly lower than the default switching frequency. As the output current demand increase the bursts become more frequent until the module automatically transitions out of PFM operation.

The burst frequency is a function of input voltage, output voltage, output current, C_{FF} , and C_{OUT} . Changing any of these parameters will alter the device's behavior during PFM operation.

171010550	
Before Change	After Change
<p>MODES OF OPERATION</p> <p>The MODE pin of the 171010550 can be pulled either high or low to alter the light load performance of the module based on the application requirements. When pulled high, COT operation will be forced throughout the entire load current range. When pulled low, power save operation will occur during light load conditions. The MicroModule will operate in one of four modes, depending on the operating conditions.</p> <p>Constant on-time (COT) operation</p> <p>The MicroModule operates at a fixed switching frequency of 4MHz where the duty cycle (DC) is determined by the following equation:</p> $DC = \frac{V_{OUT}}{V_{IN}} \quad (4)$ <p>The on-time is determined by the duty cycle and the switching frequency as follows:</p> $t_{ON} = \frac{DC}{f_{SW}} \quad (5)$ <p>The on and off-times can be related to the switching frequency as follows:</p> $\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$ <p>This mode of operation has a minimum off-time value of 60ns.</p> <p>Fixed off-time operation</p> <p>When the minimum off-time of 60ns is reached and the duty cycle must increase further, the MicroModule fixes the off-time to 60ns and begins increasing the on-time. This results in a decrease in switching frequency proportional to the increase in duty cycle.</p> <p>100% duty cycle operation</p> <p>When the input voltage approaches the output voltage and the duty cycle approaches 100%, the MicroModule will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.</p> <p>Power save operation</p> <p>Power save operation is initiated when the MicroModule enters discontinuous mode, typically occurring between 0mA and 300mA. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency.</p>	<p>15 MODES OF OPERATION</p> <p>The MODE pin of the 171010550 can be pulled either high or low to alter the light load performance of the power module based on the application requirements. When pulled high, COT operation will be forced throughout the entire load current range. When pulled low, PFM operation will occur during light load conditions. The power module will operate in one of four modes, depending on the operating conditions.</p> <p>15.1 Constant On-Time (COT) Operation</p> <p>The power module operates at a fixed switching frequency of 4MHz where the duty cycle (D) is determined by the following equation:</p> $D = \frac{V_{OUT}}{V_{IN}} \quad (4)$ <p>The on-time is determined by the duty cycle and the switching frequency as follows:</p> $t_{ON} = \frac{D}{f_{SW}} \quad (5)$ <p>The on and off-times can be related to the switching frequency as follows:</p> $\frac{1}{f_{SW}} = t_{ON} + t_{OFF} \quad (6)$ <p>This mode of operation has a minimum off-time value of 60ns.</p> <p>15.2 Fixed Off-Time Operation</p> <p>When the minimum off-time of 60ns is reached and the duty cycle must increase further, the power module fixes the off-time to 60ns and begins increasing the on-time. This results in a decrease in switching frequency proportional to the increase in duty cycle.</p> <p>15.3 100% Duty Cycle Operation</p> <p>When the input voltage approaches the output voltage and the duty cycle approaches 100%, the power module will leave the high side MOSFET on continuously and the output voltage will be limited by the input voltage. Further decreases of input voltage will result in a corresponding decrease in output voltage.</p> <p>15.4 Pulse Frequency Modulation (PFM) Operation</p> <p>If the MODE pin is pulled low PFM operation is initiated when the power module enters discontinuous mode. A burst of switching cycles increases the output voltage above the set value followed by a period of dead time where the output current is only delivered by the output capacitor. This results in slightly increased output voltage ripple in exchange for significantly increased conversion efficiency. The frequency of the bursts depends on the load and is significantly lower than the default switching frequency. As the output current demand increase the bursts become more frequent until the module automatically transitions out of PFM operation. The burst frequency is a function of input voltage, output voltage, output current, C_{FF}, and C_{OUT}. Changing any of these parameters will alter the device's behavior during PFM operation.</p>

4. Overcurrent graph has been updated

171960501	
Before Change	After Change
<p>Overcurrent protection (OCP)</p> <p>For protection against load faults, the Magi²C MicroModule incorporates cycle-by-cycle current limiting (see I_{OCP} in "Electrical Specification" on page 5). During an overcurrent condition the output current is limited and the output voltage drops (see figure below). When the overcurrent condition is removed, the output voltage returns to the nominal voltage.</p> 	<p>17.1 Overcurrent Protection (OCP)</p> <p>For protection against load faults, the 171960501 Magi²C power module incorporates a current limit (see I_{OCP} in ELECTRICAL SPECIFICATION). During an overcurrent condition the output current is limited and the output voltages drops (see figure below). When the overcurrent condition is removed, the output voltage returns to the nominal voltage.</p>  <p style="text-align: center;">Figure 21: 171960501 OCP V_{IN} = 3.6V, V_{OUT} = 1.8V.</p>



5. Updated handling recommendations section to new formatting

171960501

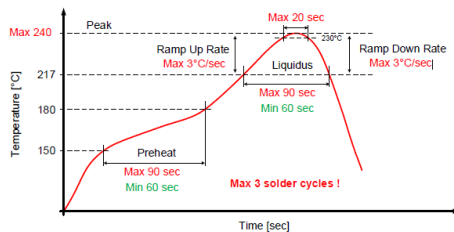
Before Change

HANDLING RECOMMENDATIONS

1. The power MicroModule is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033).
2. The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year.
3. When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card.
4. Parts must be processed after 168 hours (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation.

SOLDER PROFILE

1. Only Pb-Free assembly is recommended according to JEDEC J-STD020.
2. Measure the peak reflow temperature of the MagPC MicroModule in the middle of the packaged IC on top.
3. Ensure that the peak reflow temperature does not exceed 235°C ±5°C as per JEDEC J-STD020.
4. The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds.
5. Reflow time above liquidus (217°C) must not exceed 90 seconds.
6. Maximum ramp up rate is 3°C per second.
7. Maximum ramp down rate is 3°C per second.
8. Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020.
9. **Maximum number of allowed reflow cycles is three.**
10. **For minimum risk, solder the MicroModule in the last reflow cycle of the PCB production.**
11. For soldering process please consider lead material copper (Cu) and lead finish tin (Sn).
12. For solder paste use a SAC 305 alloy (Sn 96.5 / Ag 3.0 / Cu 0.5) type 3 or higher.
13. The profile shown below is valid for convection reflow only.
14. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk.



After Change

20 HANDLING RECOMMENDATIONS

1. The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033.
2. The components should be sealed and stored in a controlled environment prior to soldering or other use.
3. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon.
4. Maximum numbers of reflow cycles is two.
5. For minimum risk, solder the module in the last reflow cycle of the PCB production.
6. Please consider that the leads are finished with AgPd.
7. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
8. The profile below is valid for convection reflow only.
9. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk.

21 SOLDERING PROFILE

Table 13: Reflow soldering profile.

Profile Feature	Symbol	Value
Preheat temperature minimum	T_{s_min}	150°C
Preheat temperature maximum	T_{s_max}	180°C
Preheat time from T_{s_min} to T_{s_max}	t_p	60-90 seconds
Liquidous temperature	T_L	217°C
Time maintained above T_L	t_L	60-90 seconds
Classification temperature	T_C	240°C
Peak package body temperature	T_P	$T_P \leq T_C$
Time within $T_C - 5^\circ\text{C}$ and T_C	t_p	$t_p \leq 20$ seconds
Ramp-up rate (T_P to T_L)		3°C/second maximum
Ramp-down rate (T_P to T_L)		3°C/second maximum
Time 25°C to peak temperature		8 minutes maximum

Please refer to JEDEC J-STD020E for further information pertaining to reflow soldering of electronic components.

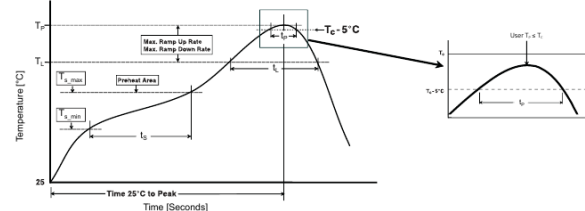


Figure 27: Soldering profile.



171010501

Before Change

After Change

HANDLING RECOMMENDATIONS

- The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033).
- The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year.
- When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card.
- Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation.

SOLDER PROFILE

- Measure the peak reflow temperature of the Mag¹PC power module in the middle of the top view.
- Ensure that the peak reflow temperature does not exceed 235°C ±5°C.
- The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds.
- Reflow time above liquidus (217°C) must not exceed 90 seconds.
- Maximum ramp up is rate 3K per second
- Maximum ramp down rate is 3K per second
- Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020.
- Maximum numbers of reflow cycles is three.
- For minimum risk, solder the module in the last reflow cycle of the PCB production.
- For soldering process please consider lead material silver (Ag) and palladium (Pd).
- For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
- Below profile is valid for convection reflow only
- Other soldering methods (e.g.vapor phase) are not verified and have to be validated by the customer on his own risk

19 HANDLING RECOMMENDATIONS

- The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033.
- The components should be sealed and stored in a controlled environment prior to soldering or other use.
- If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon.
- Maximum numbers of reflow cycles is two.
- For minimum risk, solder the module in the last reflow cycle of the PCB production.
- Please consider that the leads are finished with AgPd.
- For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
- The profile below is valid for convection reflow only.
- Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk

20 SOLDERING PROFILE

Table 13: Reflow soldering profile.

Profile Feature	Symbol	Value
Preheat temperature minimum	T _{s,min}	150°C
Preheat temperature maximum	T _{s,max}	180°C
Preheat time from T _{s,min} to T _{s,max}	t _s	60-90 seconds
Liquidus temperature	T _L	217°C
Time maintained above T _L	t _L	60-90 seconds
Classification temperature	T _C	240°C
Peak package body temperature	T _P	T _P ≤ T _C
Time within T _C - 5°C and T _C	t _P	t _P ≤ 20 seconds
Ramp-up Rate (T _L to T _P)		3°C/second maximum
Ramp-down rate (T _P to T _L)		3°C/second maximum
Time 25°C to peak temperature		8 minutes maximum

Please refer to JEDEC J-STD020E for further information pertaining to reflow soldering of electronic components.

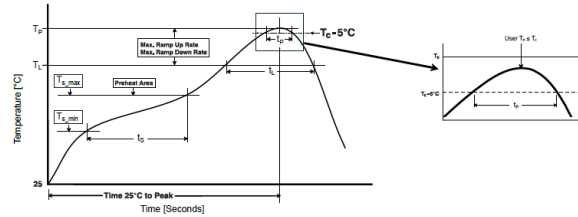


Figure 21: Soldering profile.

171010502

Before Change

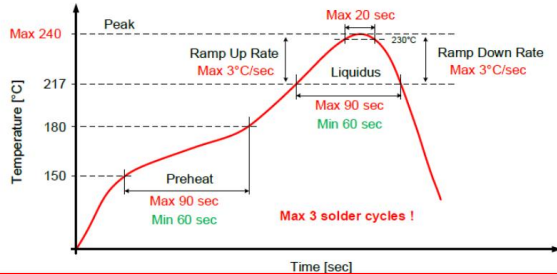
After Change

HANDLING RECOMMENDATIONS

1. The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033).
2. The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year.
3. When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card.
4. Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation.

SOLDER PROFILE

1. Measure the peak reflow temperature of the Mag1³C power module in the middle of the top view.
2. Ensure that the peak reflow temperature does not exceed 235°C ±5°C.
3. The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds.
4. Reflow time above liquidus (217°C) must not exceed 90 seconds.
5. Maximum ramp up is rate 3K per second
6. Maximum ramp down rate is 3K per second
7. Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020.
8. Maximum numbers of reflow cycles is three.
9. For minimum risk, solder the module in the last reflow cycle of the PCB production.
10. For soldering process please consider lead material silver (Ag) and palladium (Pd).
11. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
12. Below profile is valid for convection reflow only
13. Other soldering methods (e.g.vapor phase) are not verified and have to be validated by the customer on his own risk



19 HANDLING RECOMMENDATIONS

1. The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033.
2. The components should be sealed and stored in a controlled environment prior to soldering or other use.
3. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon.
4. Maximum numbers of reflow cycles is two.
5. For minimum risk, solder the module in the last reflow cycle of the PCB production.
6. Please consider that the leads are finished with AgPd.
7. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
8. The profile below is valid for convection reflow only.
9. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk

20 SOLDERING PROFILE

Table 13: Reflow soldering profile.

Profile Feature	Symbol	Value
Preheat temperature minimum	$T_{s, min}$	150°C
Preheat temperature maximum	$T_{s, max}$	180°C
Preheat time from $T_{s, min}$ to $T_{s, max}$	t_p	60-90 seconds
Liquidous temperature	T_L	217°C
Time maintained above T_L	t_L	60-90 seconds
Classification temperature	T_C	240°C
Peak package body temperature	T_{pb}	$T_{pb} \leq T_C$
Time within $T_C - 5^\circ\text{C}$ and T_C	t_{20}	$t_{20} \leq 20$ seconds
Ramp-up rate (T_L to T_C)		3°C/second maximum
Ramp-down rate (T_C to T_L)		3°C/second maximum
Time 25°C to peak temperature		8 minutes maximum

Please refer to JEDEC J-STD020E for further information pertaining to reflow soldering of electronic components.

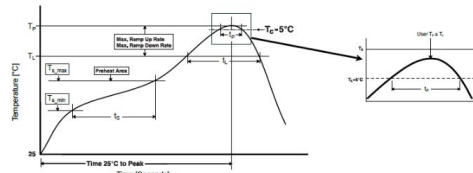


Figure 26: Soldering profile.



171010550

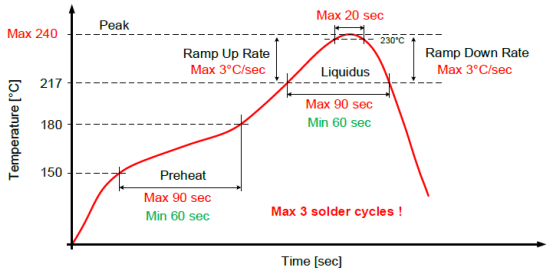
Before Change

HANDLING RECOMMENDATIONS

- The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033).
- The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year.
- When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card.
- Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation.

SOLDER PROFILE

- Only Pb-Free assembly is recommended according to JEDEC J-STD020.
- Measure the peak reflow temperature of the Mag13°C power module in the middle of the top view.
- Ensure that the peak reflow temperature does not exceed 235°C ±5°C as per JEDEC J-STD020.
- The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds.
- Reflow time above liquidus (217°C) must not exceed 60 seconds.
- Maximum ramp up is rate 3°C per second
- Maximum ramp down rate is 3°C per second
- Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020.
- Maximum numbers of reflow cycles is two.
- For minimum risk, solder the module in the last reflow cycle of the PCB production.
- For soldering process please consider lead material copper (Cu) and lead finish tin (Sn).
- For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
- Below profile is valid for convection reflow only
- Other soldering methods (e.g.vapor phase) are not verified and have to be validated by the customer on his own risk



After Change

19 HANDLING RECOMMENDATIONS

- The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033D).
- The parts are delivered in a sealed bag (Moisture Barrier Bag = MBB) and should be processed within one year.
- When opening the moisture barrier bag, check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card.
- Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033D recommendation.
- Maximum number of solder cycles is two.
- For minimum risk, solder the module in the last solder cycle of the PCB production.
- Please consider that the leads are finished with NiPdAu.
- It is recommended to use a standard SAC Alloy such as SAC 305, type 3 or higher.
- The profile below is valid for convection reflow only.
- Vapor phase soldering following the reflow soldering profile has been experimentally verified.
- Other soldering methods and profiles for soldering have not been verified and have to be validated by the customer at their own risk.

20 SOLDERING PROFILE

Table 13: Reflow soldering profile.

Profile Feature	Symbol	Value
Preheat temperature minimum	T _{s,min}	150°C
Preheat temperature maximum	T _{s,max}	180°C
Preheat time from T _{s,min} to T _{s,max}	t _s	60-90 seconds
Liquidous temperature	T _L	217°C
Time maintained above T _L	t _L	60-90 seconds
Classification temperature	T _C	260°C
Peak package body temperature	T _P	T _P ≤ T _C
Time within T _C - 5°C and T _C	t _P	t _P ≤ 20 seconds
Ramp-up rate (T _L to T _P)		3°C/second maximum
Ramp-down rate (T _P to T _L)		3°C/second maximum
Time 25°C to peak temperature		8 minutes maximum

Please refer to JEDEC J-STD020E for further information pertaining to reflow soldering of electronic components.

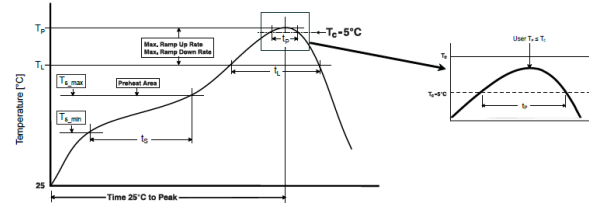


Figure 25: Soldering profile.

Changes 6 through 8 apply only to 171960501, 171010501 and 171010502.

6. MSL level changed from 3 to 1

171960501

Before Change

HANDLING RECOMMENDATIONS

- The power MicroModule is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033).
- The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year.
- When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card.
- Parts must be processed after 168 hours (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation.

After Change

20 HANDLING RECOMMENDATIONS

- The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD-020F.
- The components should be sealed and stored in a controlled environment prior to soldering or other use.
- If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon.
- Maximum numbers of reflow cycles is three.
- For minimum risk, solder the module in the last reflow cycle of the PCB production.
- The component lead material is silver (Ag) and palladium (Pd).
- For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher.
- Below profile is valid for convection reflow only.
- Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk



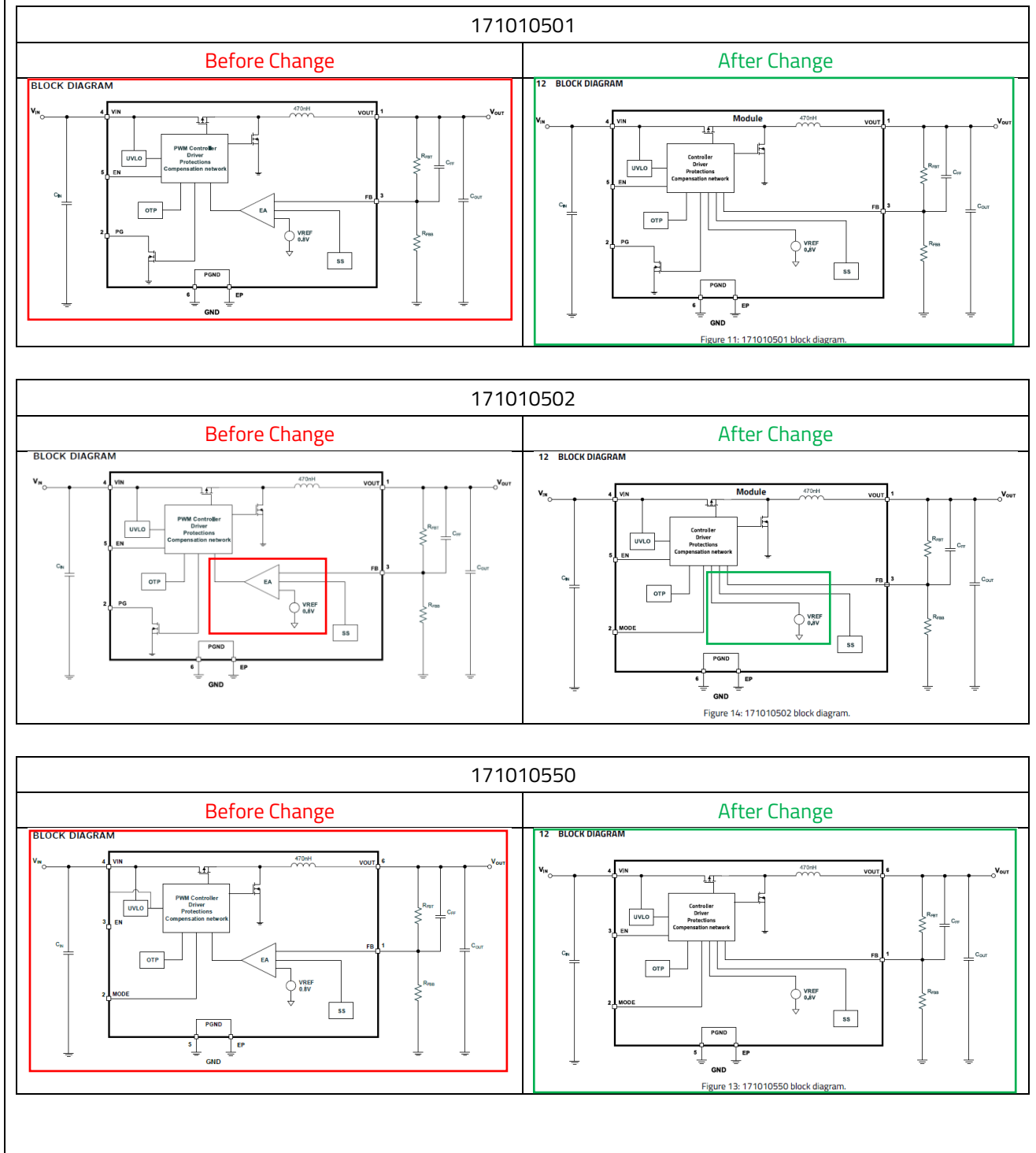
171010501	
Before Change	After Change
<p>HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033). The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year. When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card. Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation. 	<p>19 HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. The components should be sealed and stored in a controlled environment prior to soldering or other use. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. Maximum numbers of reflow cycles is two. For minimum risk, solder the module in the last reflow cycle of the PCB production. Please consider that the leads are finished with AgPd. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. The profile below is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk
171010502	
Before Change	After Change
<p>HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033). The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year. Ensure that the peak reflow temperature does not exceed 235°C ±5°C as per JEDEC J-STD020. Indicator color has changed according to the notes on the card. Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation. 	<p>19 HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. The components should be sealed and stored in a controlled environment prior to soldering or other use. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. Maximum numbers of reflow cycles is two. For minimum risk, solder the module in the last reflow cycle of the PCB production. Please consider that the leads are finished with AgPd. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. The profile below is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk
<p>7. Reduced number of allowable solder cycles from 3 to 2</p>	
171960501	
Before Change	After Change
<p>SOLDER PROFILE</p> <ol style="list-style-type: none"> Only Pb-Free assembly is recommended according to JEDEC J-STD020. Measure the peak reflow temperature of the MagPC MicroModule in the middle of the packaged IC on top. Ensure that the peak reflow temperature does not exceed 235°C ±5°C as per JEDEC J-STD020. The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds. Reflow time above liquidus (217°C) must not exceed 90 seconds. Maximum ramp up rate is 3°C per second. Maximum ramp down rate is 3°C per second. Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020. Maximum number of allowed reflow cycles is three. For minimum risk, solder the MicroModule in the last reflow cycle of the PCB production. For soldering process please consider lead material copper (Cu) and lead finish tin (Sn). For solder paste use a SAC 305 alloy (Sn 96.5 / Ag 3.0 / Cu 0.5) type 3 or higher. The profile shown below is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk. 	<p>20 HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD-020F. The components should be sealed and stored in a controlled environment prior to soldering or other use. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. Maximum numbers of reflow cycles is two. For minimum risk, solder the module in the last reflow cycle of the PCB production. The component lead material is silver (Ag) and palladium (Pd). For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. Below profile is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk
171010501	
Before Change	After Change
<p>SOLDER PROFILE</p> <ol style="list-style-type: none"> Measure the peak reflow temperature of the MagPC power module in the middle of the top view. Ensure that the peak reflow temperature does not exceed 235°C ±5°C. The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds. Reflow time above liquidus (217°C) must not exceed 90 seconds. Maximum ramp up rate is 3K per second Maximum ramp down rate is 3K per second Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020. Maximum numbers of reflow cycles is three. For minimum risk, solder the module in the last reflow cycle of the PCB production. For soldering process please consider lead material silver (Ag) and palladium (Pd). For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. Below profile is valid for convection reflow only Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer on his own risk 	<p>19 HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. The components should be sealed and stored in a controlled environment prior to soldering or other use. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. Maximum numbers of reflow cycles is two. For minimum risk, solder the module in the last reflow cycle of the PCB production. Please consider that the leads are finished with AgPd. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. The profile below is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk



171010502	
Before Change	After Change
SOLDER PROFILE <ol style="list-style-type: none"> 1. Measure the peak reflow temperature of the Mag³C power module in the middle of the top view. 2. Ensure that the peak reflow temperature does not exceed 235°C ±5°C. 3. The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds. 4. Reflow time above liquidus (217°C) must not exceed 90 seconds. 5. Maximum ramp up is rate 3K per second 6. Maximum ramp down rate is 3K per second 7. Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020. 8. Maximum numbers of reflow cycles is three. 9. For minimum risk, solder the module in the last reflow cycle of the PCB production. 10. For soldering process please consider lead material silver (Ag) and palladium (Pd). 11. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. 12. Below profile is valid for convection reflow only 13. Other soldering methods (e.g.vapor phase) are not verified and have to be validated by the customer on his own risk 	19 HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. 2. The components should be sealed and stored in a controlled environment prior to soldering or other use. 3. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. 4. Maximum numbers of reflow cycles is two. 5. For minimum risk, solder the module in the last reflow cycle of the PCB production. 6. Please consider that the leads are finished with AgPd. 7. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. 8. The profile below is valid for convection reflow only. 9. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk
8. Added statements about open-air storage of components and baking within inert gas	
171960501	
Before Change	After Change
HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power MicroModule is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033). 2. The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year. 3. When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card. 4. Parts must be processed after 168 hours (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation. 	20 HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. 2. The components should be sealed and stored in a controlled environment prior to soldering or other use. 3. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. 4. Maximum numbers of reflow cycles is two. 5. For minimum risk, solder the module in the last reflow cycle of the PCB production. 6. Please consider that the leads are finished with AgPd. 7. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. 8. The profile below is valid for convection reflow only. 9. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk
171010501	
Before Change	After Change
HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033). 2. The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year. 3. When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card. 4. Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation. 	19 HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. 2. The components should be sealed and stored in a controlled environment prior to soldering or other use. 3. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. 4. Maximum numbers of reflow cycles is two. 5. For minimum risk, solder the module in the last reflow cycle of the PCB production. 6. Please consider that the leads are finished with AgPd. 7. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. 8. The profile below is valid for convection reflow only. 9. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk
171010502	
Before Change	After Change
HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power module is classified as MSL3 (JEDEC Moisture Sensitivity Level 3) and requires special handling due to moisture sensitivity (JEDEC J-STD033). 2. The parts are delivered in a sealed bag (Moisture Barrier Bags = MBB) and should be processed within one year. 3. When opening the moisture barrier bag check the Humidity Indicator Card (HIC) for color status. Bake parts prior to soldering in case indicator color has changed according to the notes on the card. 4. Parts must be processed after 168 hour (7 days) of floor life. Once this time has been exceeded, bake parts prior to soldering per JEDEC J-STD033 recommendation. 	19 HANDLING RECOMMENDATIONS <ol style="list-style-type: none"> 1. The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. 2. The components should be sealed and stored in a controlled environment prior to soldering or other use. 3. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. 4. Maximum numbers of reflow cycles is two. 5. For minimum risk, solder the module in the last reflow cycle of the PCB production. 6. Please consider that the leads are finished with AgPd. 7. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. 8. The profile below is valid for convection reflow only. 9. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk

Change 9 applies only to 171010501, 171010502, and 171010550

9. Block diagram has been updated





Changes 10 and 11 apply only to 171010501 and 171010502

10. Absolute maximum voltage ratings have been corrected

171010501																																																																																									
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11. Feedback resistor value in the design flow table has been corrected

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Changes 12 and 13 apply only to 171960501

12. Lead finish information has been corrected

171960501	
Before Change	After Change
<p>SOLDER PROFILE</p> <ol style="list-style-type: none"> Only Pb-Free assembly is recommended according to JEDEC J-STD020. Measure the peak reflow temperature of the MagPC MicroModule in the middle of the packaged IC on top. Ensure that the peak reflow temperature does not exceed 235°C ±5°C as per JEDEC J-STD020. The reflow time period during peak temperature of 235°C ±5°C must not exceed 20 seconds. Reflow time above liquidus (217°C) must not exceed 90 seconds. Maximum ramp up rate is 3°C per second. Maximum ramp down rate is 3°C per second. Reflow time from room (25°C) to peak must not exceed 8 minutes as per JEDEC J-STD020. Maximum number of allowed reflow cycles is three. For minimum risk, solder the MicroModule in the last reflow cycle of the PCB production. For soldering process please consider lead material copper (Cu) and lead finish tin (Sn). For solder paste use a SAC 305 alloy (Sn 96.5 / Ag 3.0 / Cu 0.5) type 3 or higher. The profile shown below is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk. 	<p>20 HANDLING RECOMMENDATIONS</p> <ol style="list-style-type: none"> The power module is classified as MSL1 (JEDEC Moisture Sensitivity Level 1) according to JEDEC J-STD033. The components should be sealed and stored in a controlled environment prior to soldering or other use. If the components are to be baked prior to soldering, the baking must be performed with the components in an inert gas such as nitrogen or argon. Maximum numbers of reflow cycles is two. For minimum risk, solder the module in the last reflow cycle of the PCB production. Please consider that the leads are finished with AgPd. For solder paste use a standard SAC Alloy such as SAC 305, type 3 or higher. The profile below is valid for convection reflow only. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer at their own risk.

13. Storage temperature range has been corrected

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<p>ABSOLUTE MAXIMUM RATINGS</p> <p>Caution: Exceeding the listed absolute maximum ratings may affect the device negatively and may cause permanent damage.</p> <table border="1"> <thead> <tr> <th rowspan="2">SYMBOL</th> <th rowspan="2">PARAMETER</th> <th colspan="2">LIMITS</th> <th rowspan="2">UNIT</th> </tr> <tr> <th>MIN ⁽¹⁾</th> <th>MAX ⁽¹⁾</th> </tr> </thead> <tbody> <tr> <td>V_{IN}</td> <td>Input voltage pin</td> <td>-0.3</td> <td>6</td> <td>V</td> </tr> <tr> <td>V_{OUT}</td> <td>Output voltage pin</td> <td>-0.3</td> <td>V_{IN}</td> <td>V</td> </tr> <tr> <td>FB</td> <td>Feedback pin</td> <td>-0.3</td> <td>V_{IN}</td> <td>V</td> </tr> <tr> <td>EN</td> <td>Enable pin</td> <td>-0.3</td> <td>V_{IN}+0.3</td> <td>V</td> </tr> <tr> <td>MODE</td> <td>Mode pin</td> <td>-0.3</td> <td>V_{IN}</td> <td>V</td> </tr> <tr> <td>T_{storage}</td> <td>Assembled, non-operating storage temperature</td> <td>-65</td> <td>150</td> <td>°C</td> </tr> <tr> <td>V_{ESD}</td> <td>ESD voltage (HBM), V_{IN} and V_{OUT} vs. PGND (C=100pF, R= 1.5kΩ) according to AEC-Q100-002⁽³⁾</td> <td>-4</td> <td>4</td> <td>kV</td> </tr> <tr> <td>V_{ESD}</td> <td>ESD voltage (HBM), EN, MODE and FB vs. PGND (C=100pF, R= 1.5kΩ) according to AEC-Q100-002⁽³⁾</td> <td>-2</td> <td>2</td> <td>kV</td> </tr> </tbody> </table>	SYMBOL	PARAMETER	LIMITS		UNIT	MIN ⁽¹⁾	MAX ⁽¹⁾	V _{IN}	Input voltage pin	-0.3	6	V	V _{OUT}	Output voltage pin	-0.3	V _{IN}	V	FB	Feedback pin	-0.3	V _{IN}	V	EN	Enable pin	-0.3	V _{IN} +0.3	V	MODE	Mode pin	-0.3	V _{IN}	V	T _{storage}	Assembled, non-operating storage temperature	-65	150	°C	V _{ESD}	ESD voltage (HBM), V _{IN} and V _{OUT} vs. PGND (C=100pF, R= 1.5kΩ) according to AEC-Q100-002 ⁽³⁾	-4	4	kV	V _{ESD}	ESD voltage (HBM), EN, MODE and FB vs. PGND (C=100pF, R= 1.5kΩ) according to AEC-Q100-002 ⁽³⁾	-2	2	kV	<p>4 ABSOLUTE MAXIMUM RATINGS</p> <p>Caution: Exceeding the listed absolute maximum ratings may affect the device negatively and may cause permanent damage.</p> <p>Table 4: Absolute minimum ratings.</p> <table border="1"> <thead> <tr> <th rowspan="2">SYMBOL</th> <th rowspan="2">PARAMETER</th> <th colspan="2">LIMIT</th> <th rowspan="2">UNIT</th> </tr> <tr> <th>MIN⁽¹⁾</th> <th>MAX⁽¹⁾</th> </tr> </thead> <tbody> <tr> <td>V_{IN}</td> <td>Input pin voltage</td> <td>-0.3</td> <td>6</td> <td>V</td> </tr> <tr> <td>V_{OUT}</td> <td>Output pin voltage</td> <td>-0.3</td> <td>V_{IN}</td> <td>V</td> </tr> <tr> <td>FB</td> <td>Feedback pin voltage</td> <td>-0.3</td> <td>V_{IN}</td> <td>V</td> </tr> <tr> <td>EN</td> <td>Enable pin voltage</td> <td>-0.3</td> <td>V_{IN}+0.3</td> <td>V</td> </tr> <tr> <td>MODE</td> <td>Mode pin voltage</td> <td>-0.3</td> <td>V_{IN}</td> <td>V</td> </tr> <tr> <td>T_{storage}</td> <td>Assembled, non-operating storage temperature</td> <td>-40</td> <td>125</td> <td>°C</td> </tr> <tr> <td>V_{ESD}</td> <td>ESD Voltage (HBM), V_{IN} and V_{OUT} vs. PGND according to EN61000-4-2⁽⁴⁾</td> <td>-4</td> <td>4</td> <td>kV</td> </tr> <tr> <td>V_{ESD}</td> <td>ESD Voltage (HBM), EN, MODE and FB vs. PGND according to EN61000-4-2⁽⁴⁾</td> <td>-2</td> <td>2</td> <td>kV</td> </tr> </tbody> </table>	SYMBOL	PARAMETER	LIMIT		UNIT	MIN ⁽¹⁾	MAX ⁽¹⁾	V _{IN}	Input pin voltage	-0.3	6	V	V _{OUT}	Output pin voltage	-0.3	V _{IN}	V	FB	Feedback pin voltage	-0.3	V _{IN}	V	EN	Enable pin voltage	-0.3	V _{IN} +0.3	V	MODE	Mode pin voltage	-0.3	V _{IN}	V	T _{storage}	Assembled, non-operating storage temperature	-40	125	°C	V _{ESD}	ESD Voltage (HBM), V _{IN} and V _{OUT} vs. PGND according to EN61000-4-2 ⁽⁴⁾	-4	4	kV	V _{ESD}	ESD Voltage (HBM), EN, MODE and FB vs. PGND according to EN61000-4-2 ⁽⁴⁾	-2	2	kV
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Change 14 applies only to 171010550

14. Information about vapor phase soldering has been added

171010550	
Before Change	After Change
<p>14. Other soldering methods (e.g. vapor phase) are not verified and have to be validated by the customer on his own risk</p>	<p>10. Vapor phase soldering following the reflow soldering profile has been experimentally verified. 11. Other soldering methods and profiles for soldering have not been verified and have to be validated by the customer at their own risk.</p>

Reliability / Qualification of Change:

As the components themselves have not been changed, there were no reliability tests performed in conjunction with this PCN.