

# <u>SEMINAR</u> INVITATION

On  $25^{th} \& 26^{th}$  of November

WURTH ELEKTRONIK MORE THAN YOU EXPECT

## INVITATION TO THE DESIGN SEMINAR ON 25TH & 26TH OF NOVEMBER

Wurth Electronics New Zealand Ltd and Midcom cordially invite you to the free Electronic Design Seminar. Optimizing Electronics Design: Advanced Problem-Solving for On-field Challenges on the 25th & 26th of November.

Refreshments and lunch will be provided at this event, free of charge throughout the day in addition to networking opportunity.

Ready to push the boundaries of your design capabilities? This seminar offers you the opportunity to deepen your expertise.

Designed for engineers like you-those who value real, hands-on insights that can be applied to solve today's design challenges.

#### Main topics:

- Transformer Design for Manufacturing
- Switches for Experts
- Live Demo: Shielding Materials with DC-DC Converter Eval Board
- Advanced Wireless Power Transfer Design
- Differential & Common Mode EMI filter Design

#### Seminar location:

25/11/2024	Auckland, NZ	Auckland Waipuna Conference Centre
		58 Waipuna Road, Mount Wellington, Auckland
		1060, New Zealand
26/11/2024	Christchurch, NZ	Russley Gold Club and Function Centre
		428 Memorial Avenue, Burnside, Christchurch
		8053, New Zealand

Please register by 11<sup>TH</sup> of November as the number of participants is limited. You can find the registration here: <u>www.we-online.com/seminar-registration</u>

If you have any further questions, please contact: eiSos-NewZealand@we-online.com We would be pleased to welcome you to our seminar.

With kind regards Wurth Electronics New Zealand Ltd & Midcom

### AGENDA FOR THE DESIGN SEMINAR ON 25TH & 26TH OF NOVEMBER

- 09:00 09:15 Arrivals, Registration & Morning Tea
- 09:15 09:30 Introductions and Welcome
- 09:30 10:30 Transformer Design for Manufacturing
- 10:30 10:45 Bio-Break
- 10:45 11:45 Switches for Experts
- 11:45 12:30 Shielding Material Demo (Using DC-DC Converter Eval Board)\*
- 12:30 13:30 Lunch
- 13:30 14:30 Advanced Wireless Power Transfer Design
- 14:30 15:00 Afternoon Tea
- 15:00 16:00 Differential and Common Mode EMI Filter Design (With Demo)
- 16:00 16:15 Open Q&A
- 16:15 16:30 Closing Remarks and Feedback Forms