Constant-On-Time, Peak Current Mode, or Zero Delay PWM: How to Make the Right Decision?

POWER LIKE A BOSS Seminar

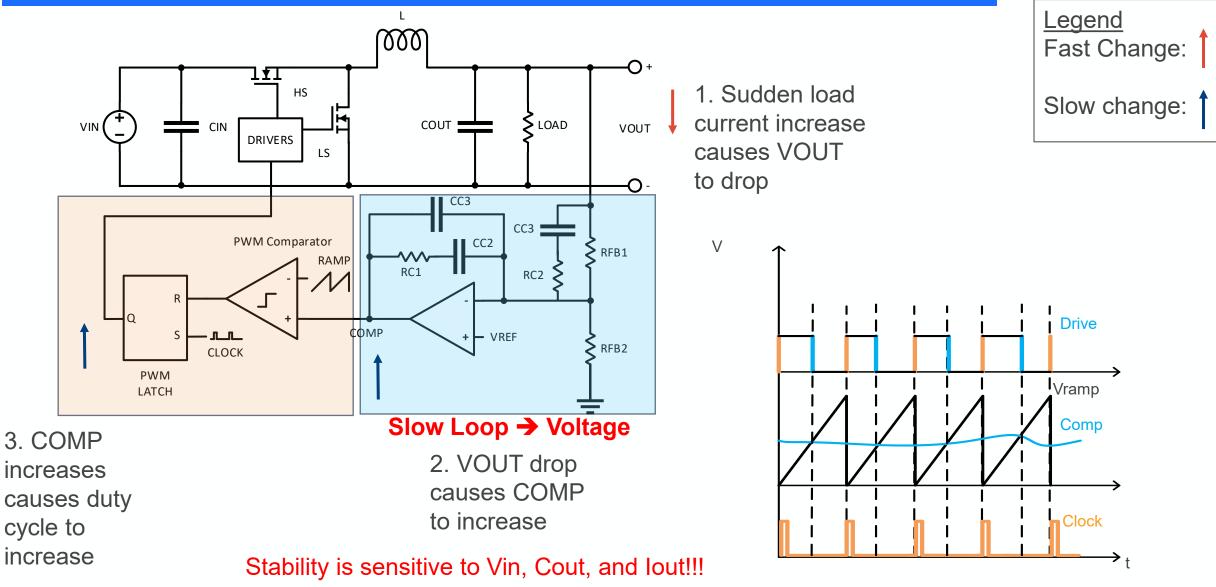
-Monolithic Power Systems/WE

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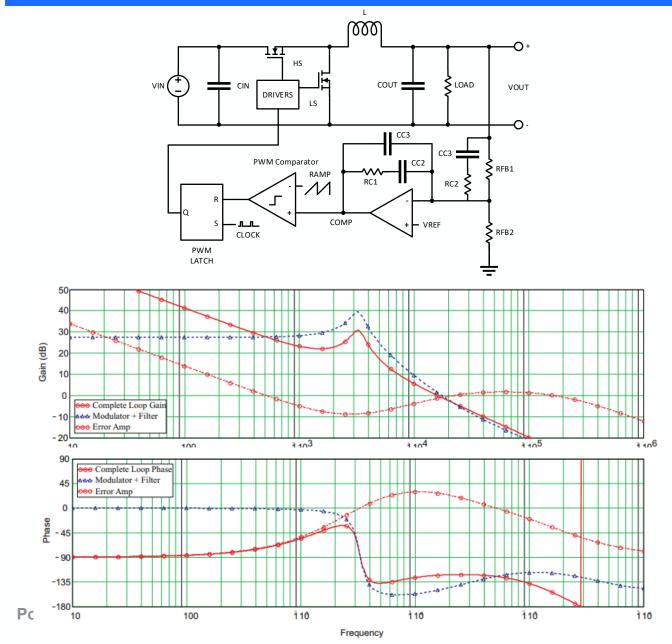


Voltage Mode Control – Method of Operation





Voltage Mode Control – Pros and Cons

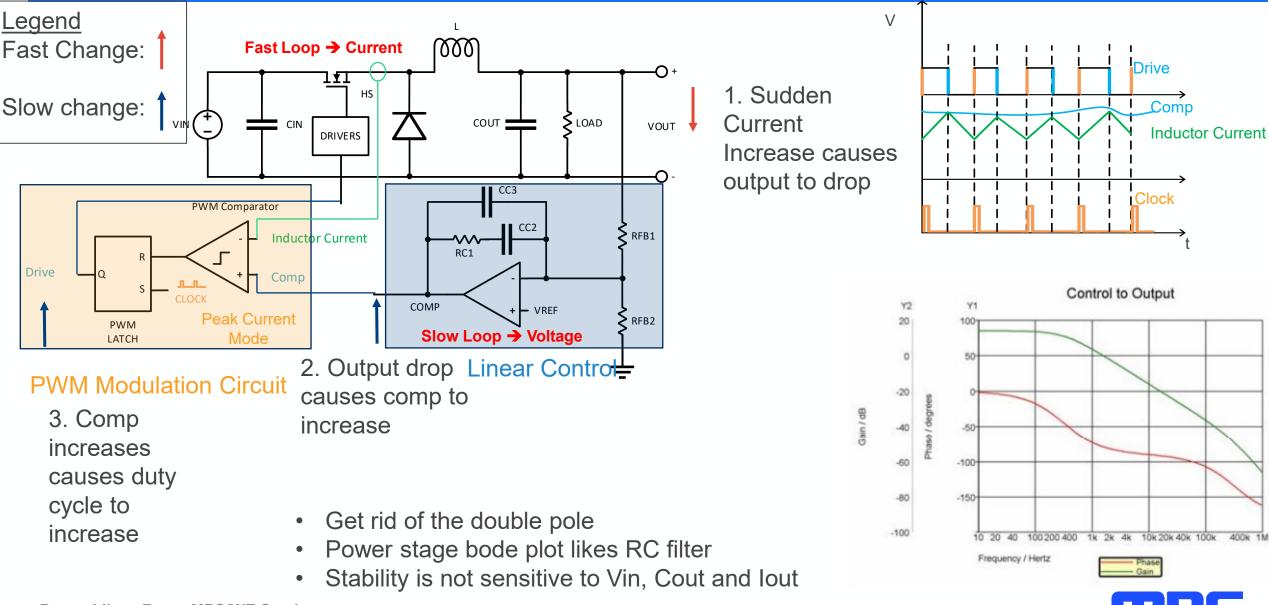


- + Simplest Control Method ©
- + Good noise immunity 😊
- Due to undamped LC output filter, requires high ESR capacitors or type III compensation 8
- Control loop gain is inversely proportional to VIN, so crossover gain will shift as VIN change. 8
- Line transient performance is poor.
 Load transient performance is okay (8)

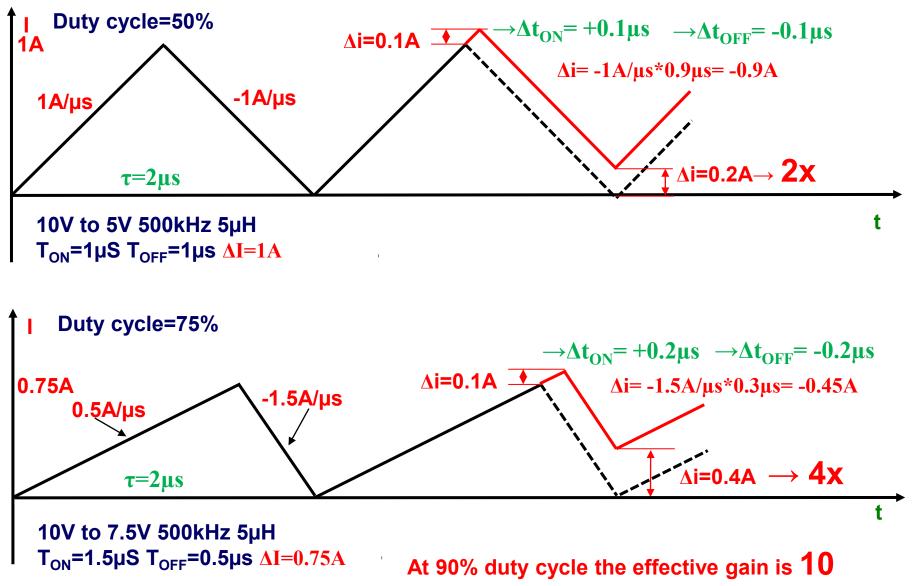
Control loop gain and line transient performance can be mitigated if ramp is made proportional to VIN.



How Is The Control Method Evolving? – Peak Current Mode Control

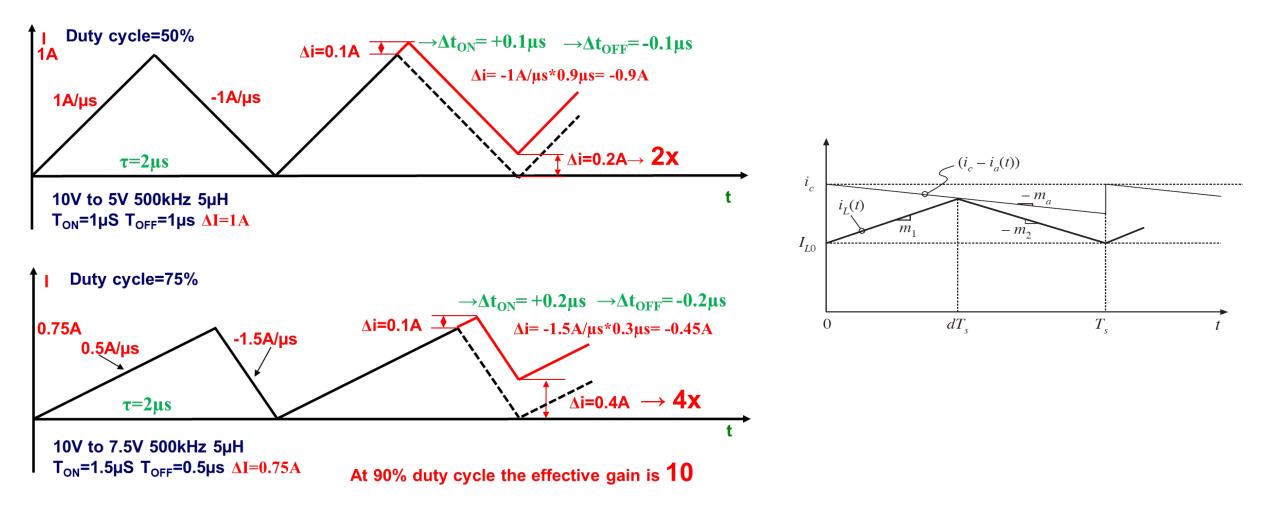


Slope Problem in <u>fixed</u> frequency <u>peak</u> Current Mode



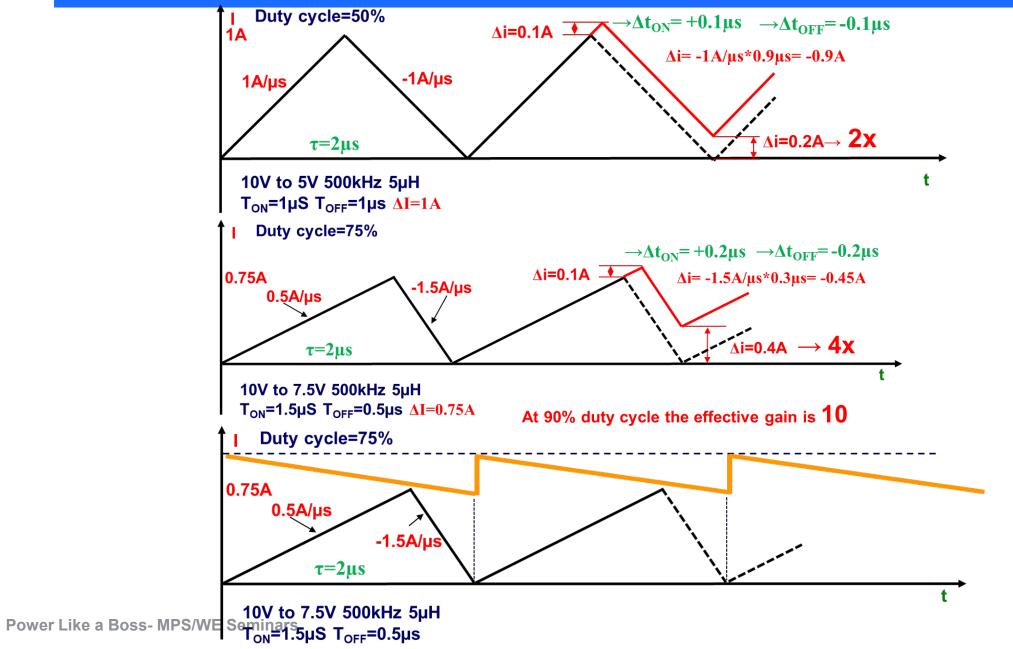


Slope Problem in <u>fixed</u> frequency <u>peak</u> Current Mode



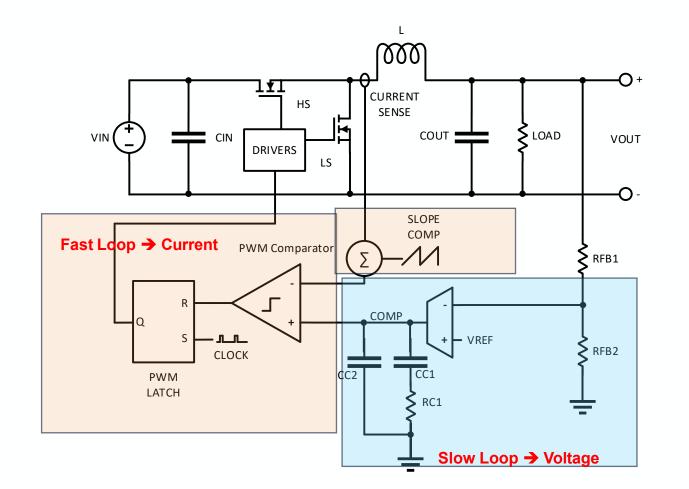


Slope Problem in <u>fixed</u> frequency <u>peak</u> Current Mode





Peak Current Mode Control – Pros and Cons



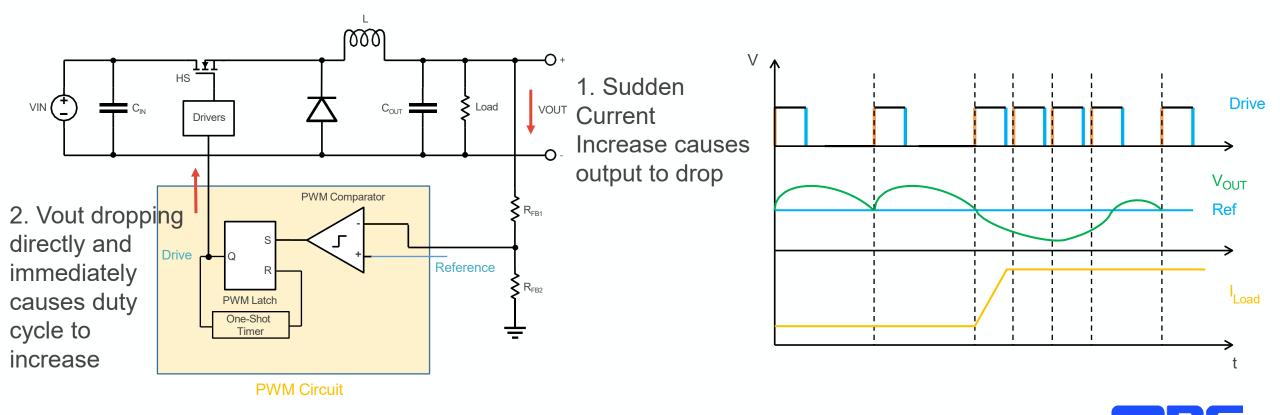
- + Current feedback acts a lossless resistor damping the output LC filter, so only type II compensation is required. ©
- + Stable crossover with Vin ©
- + Good line transient performance 😊
- Okay load transient performance 😕
- Requires current sensor 8
- Requires slope compensation for >50% duty cycle 8
- Large min on time limits minimum conversion ratio 8



Constant-On-Time (COT) Control

For applications where fast transient responses are required, current mode control is not fast enough.

- Goal: Faster transient response
- Thought Process: Transient response can be improved by not waiting for the clock.
- Solution: Use constant-on-time (COT) control to fix the on time while allowing the frequency to change.

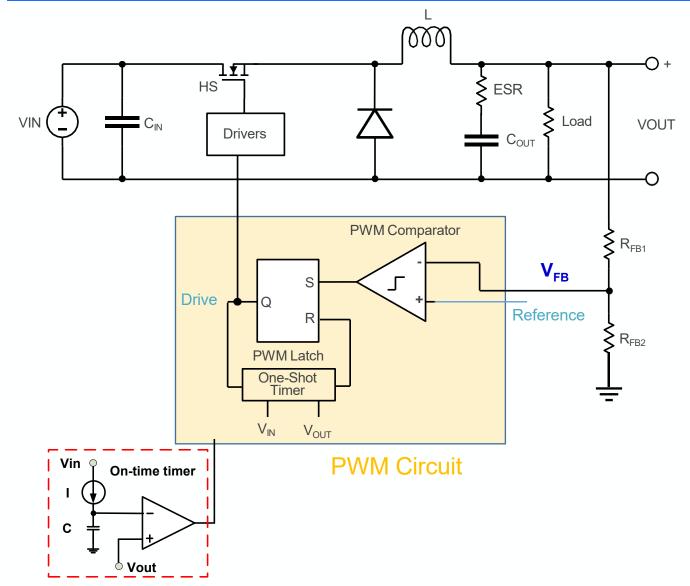


Constant-On- Time (COT) Control

Advantages	Disadvantages
 Excellent load transient performance: About 4x faster compared to fixed- 	 Must generate a slope on FB (e.g. using COUT_ESR)
frequency current mode Simple architecture does not require 	 The switching frequency (f_{SW}) is not constant due to variations in the off time Output filter design is difficult and
 compensation Seamless transition between light loads and heavy loads 	 Output filter design is difficult and undesired in many sensitive systems
 Does not require an internal oscillator 	



Solution: Adaptive COT Control



Adaptive COT control uses the conversion ratio (V_{OUT}/V_{IN}) to adjust the one-shot timer that sets the on time (t_{ON}) .

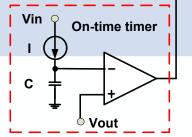
This enables a steady f_{SW} during steady state, without affecting the converter's ability to immediately change the frequency when faced with a load step.



Constant-On- Time (COT) Control

Advantages	Disadvantages
 Excellent load transient performance: About 4x faster compared to fixed- 	 Must generate a slope on FB (e.g. using COUT_ESR)
 frequency current mode Simple architecture does not require compensation 	 The switching frequency (f_{SW}) is not constant due to variations in the off time
 Seamless transition between light loads 	 Output filter design is difficult and

- Seamless transition between light loads and heavy loads
- Does not require an internal oscillator
- Quasi-stable frequency during state-state operation
- undesired in many sensitive systems



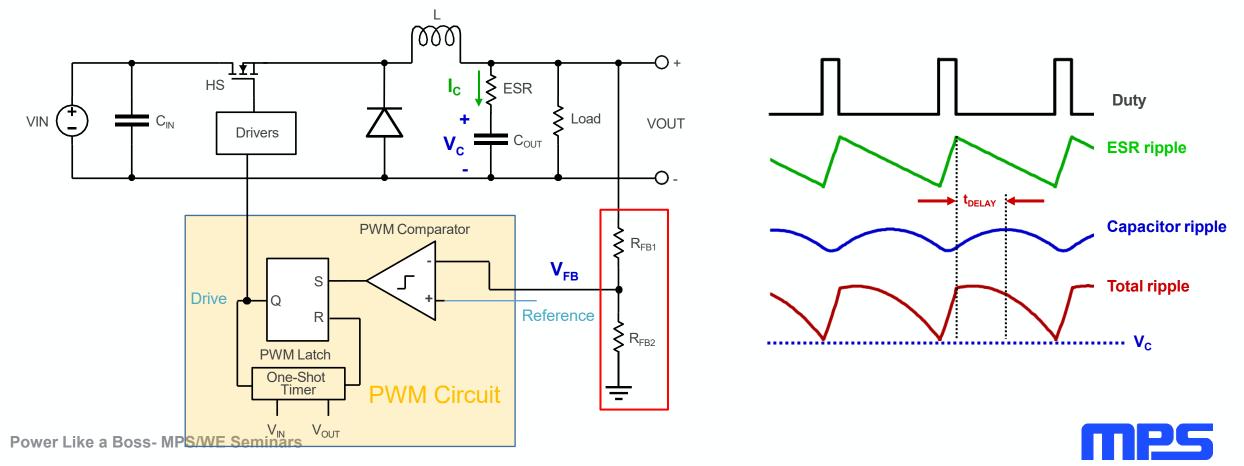


COT Challenge: Stability Is Dependent on ESR

COT control compares the feedback voltage to a set reference voltage.

The feedback voltage ripple has two main components:

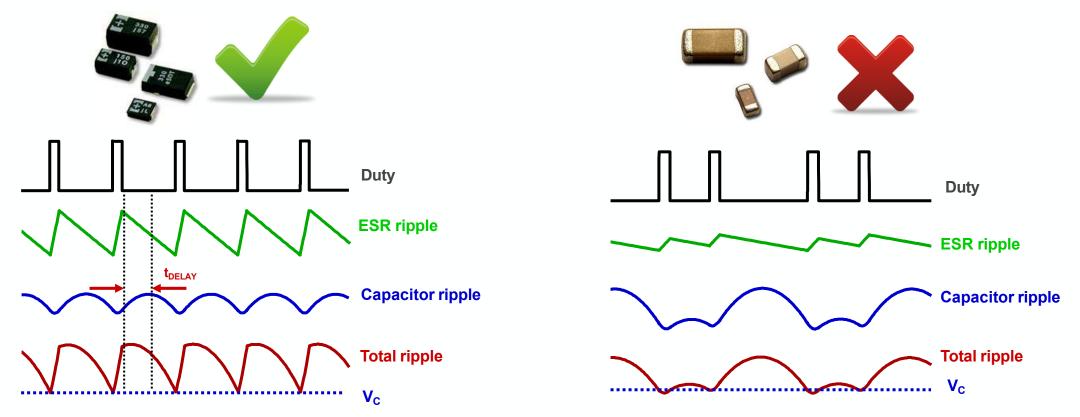
- 1. ESR ripple: Directly proportional to the inductor current (IL), with no delay/phase difference
- 2. V_{CAP} ripple: Caused by charging/discharging the output capacitor, and is delayed with respect to I_L



Solution 1: Stability Is Dependent on ESR- Solution 1

If the ESR ripple dominates, the V_{OUT} ripple is in phase with I_L , and the circuit operates correctly.

If the V_{CAP} ripple dominates, the V_{OUT} ripple is out of phase with I_L , and the circuit can enter subharmonic oscillation.

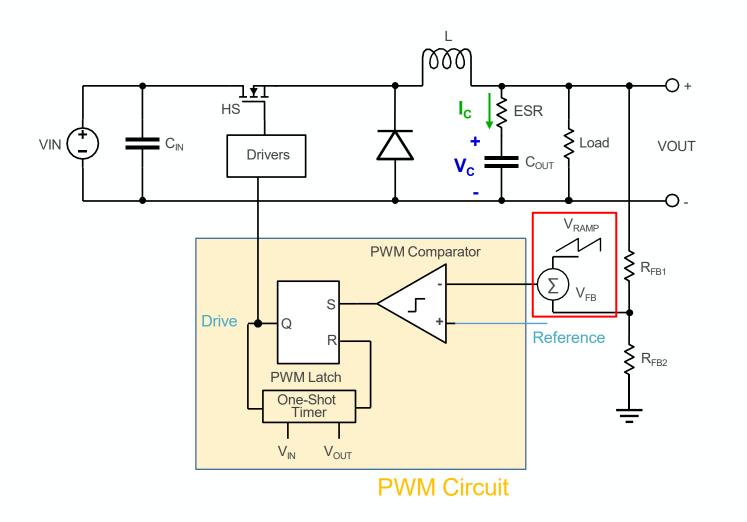


COT requires output capacitors (C_{OUT}) with large ESR for stability. If low-ESR capacitors are used (e.g. MLCC), the circuit may become unstable. Power Like a Boss- MPS/WE Seminars



MPS Solution 2: Current Ripple Injection

Option 2: Add an external ramp

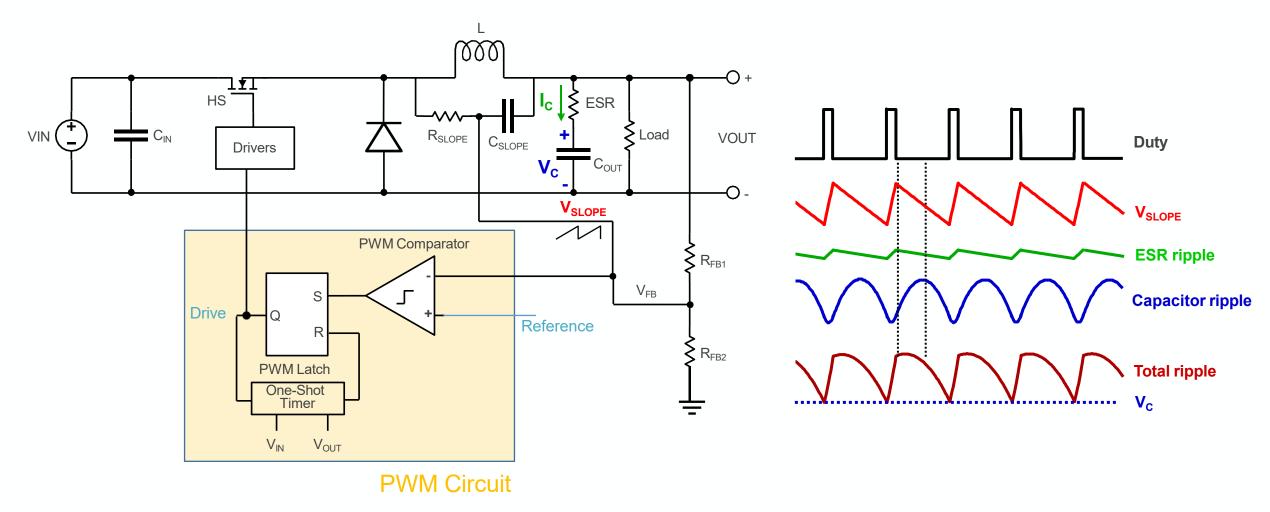


VFB' VREF



MPS Solution 3: Current Ripple Injection

Option 3: Use the RC circuit to generate a slope voltage and ensure that the FB ripple is in phase with I_L





Constant-On- Time (COT) Control

Advantages

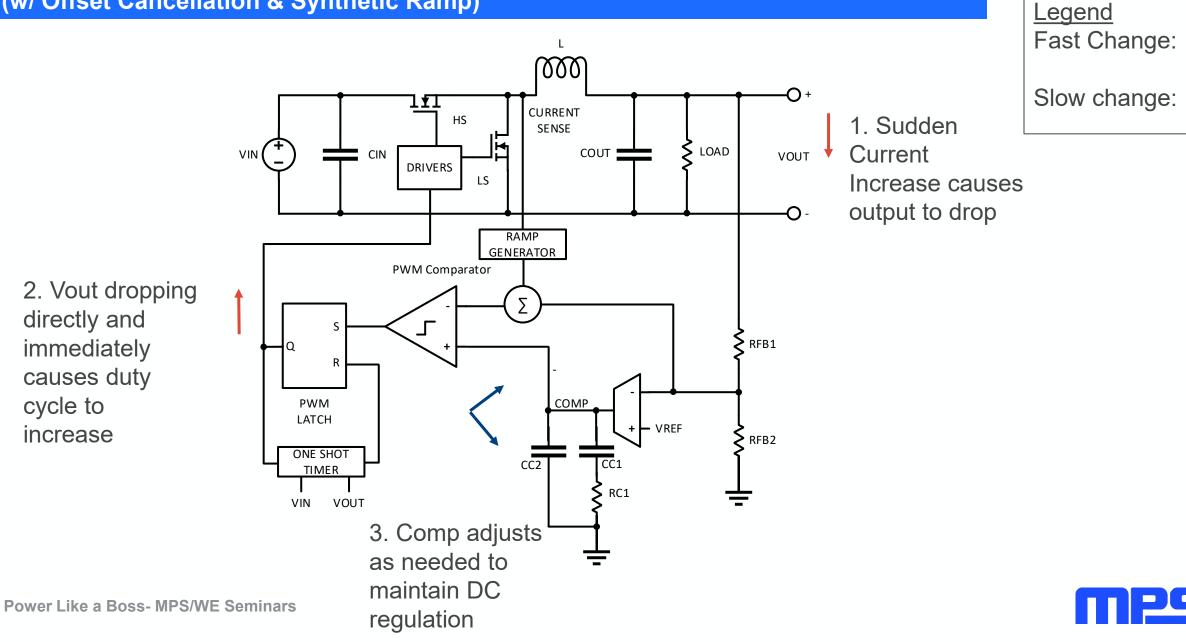
- Excellent load transient performance:
 - About 4x faster compared to fixedfrequency current mode
- Simple architecture does not require compensation
- Seamless transition between light loads and heavy loads
- Does not require an internal oscillator
- Quasi-stable frequency during state-state operation
- Integrated slope generator to keep the converter stable, even with low ESR

- Disadvantages
- Must generate a slope on FB (e.g. using COUT_ESR)
- The switching frequency (f_{sw}) is not constant due to variations in the off time
- Output filter design is difficult and undesired in many sensitive systems



Constant On-time Control - Operation

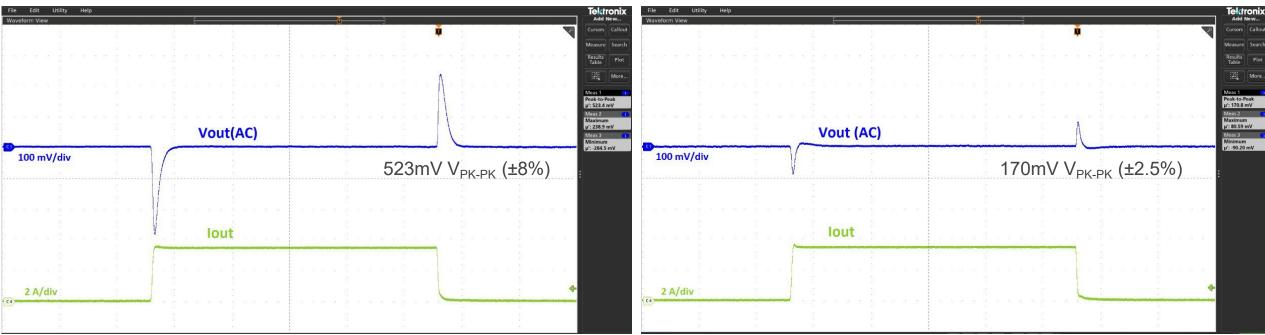
(w/ Offset Cancellation & Synthetic Ramp)



COT Advantages: Fast Transient Response

Peak Current Mode

Constant-On-Time



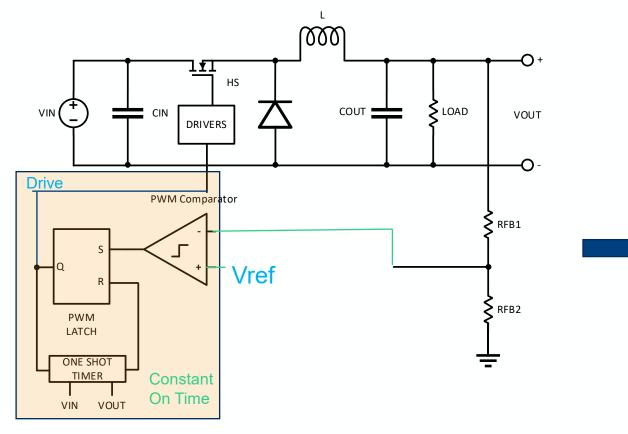
• 3x faster transient response compared to peak current mode with the same components and set-up:

 $_{\odot}$ 12V input, 3.3V output, 0A to 3.5A load step

 \circ 1 μH L_{OUT} , 2x22 μF C_{OUT}



Fastest Fix Frequency Control – ZDP



ത്ത 1¶T HS COUT S LOAD VIN CIN VOUT DRIVERS Drive **PWM** Comparator RFB1 Vref CLOCK **RFB2** PWM LATCH Zero Delay PWM (ZDP)

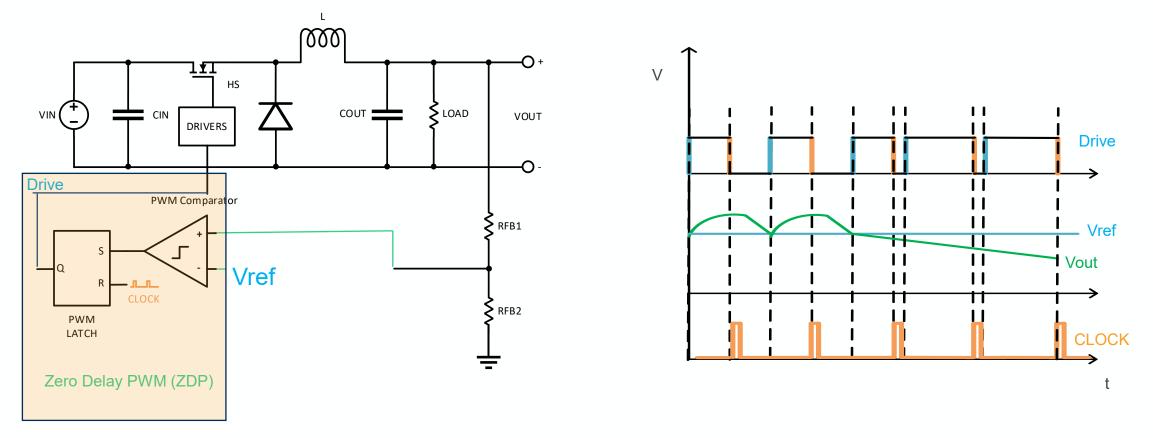
PWM Modulation Circuit

PWM Modulation Circuit

- Use clock to set the fix frequency for turn off
- Turn on immediately when Vo is lower than Vref
- As the result, duty cycle is changed instantly to response the transient



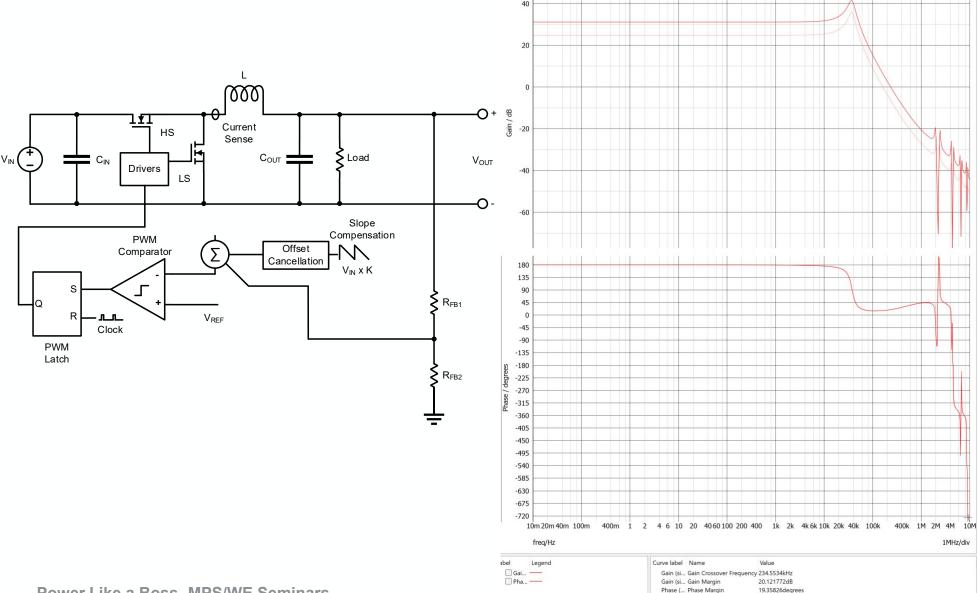
ZDP Control



PWM Modulation Circuit

- Fastest Fixed Frequency Control because the duty cycle instantly change
- No linear control loop delay

Barebone ZDP in Frequency domain



—1x Ramp (K)

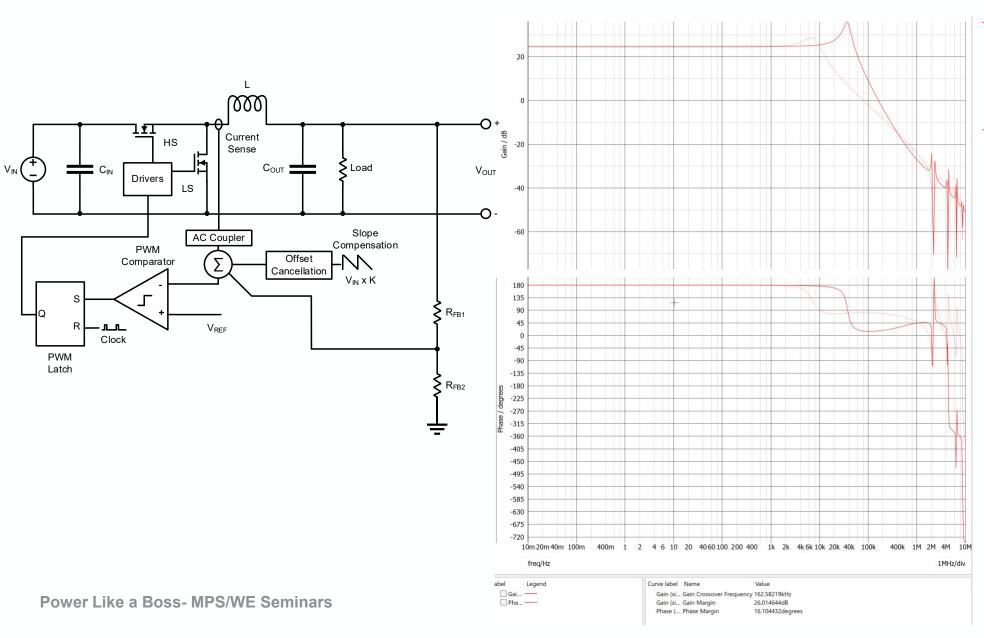
- Crossover Frequency: 234kHz
- Gain Margin: 20dB
- Phase Margin: 19 degrees

-----2x Ramp (K)

- Crossover Frequency: 163kHz
- Gain Margin: 26dB
- Phase Margin: 16 degrees



Damp the Double Pole to Reduce the Phase Drop



w/o AC coupler

- Crossover Frequency: 163kHz
- Gain Margin: 26dB
- Phase Margin: 16 degrees

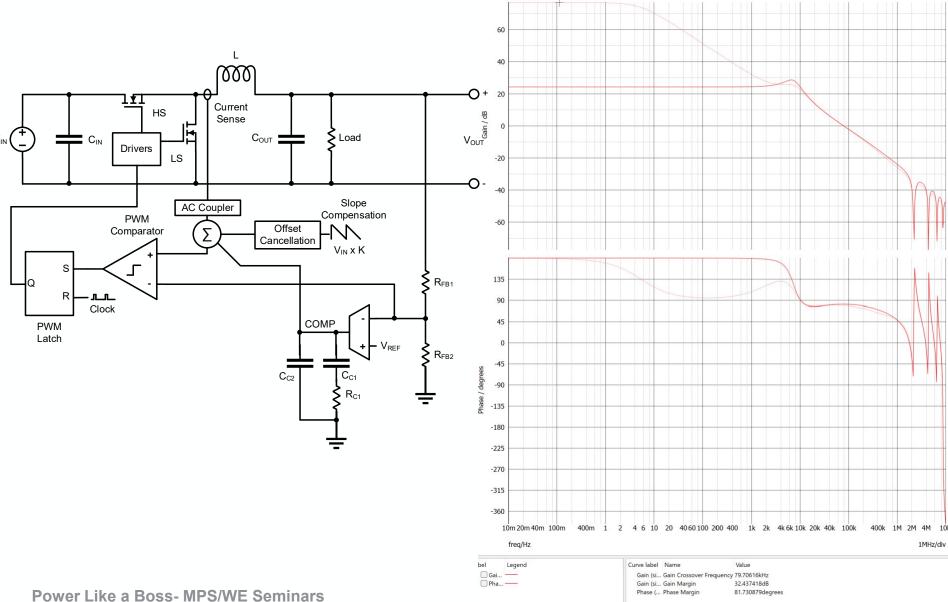
with AC coupler

- Crossover Frequency: 85kHz
- Gain Margin: 32dB
- Phase Margin: 81 degrees

Double pole is damped



Add Integrator to Reduce the DC Offset



w/o Integrator

- Crossover Frequency: 85kHz •
- Gain Margin: 32dB .
- Phase Margin: 81 degrees .

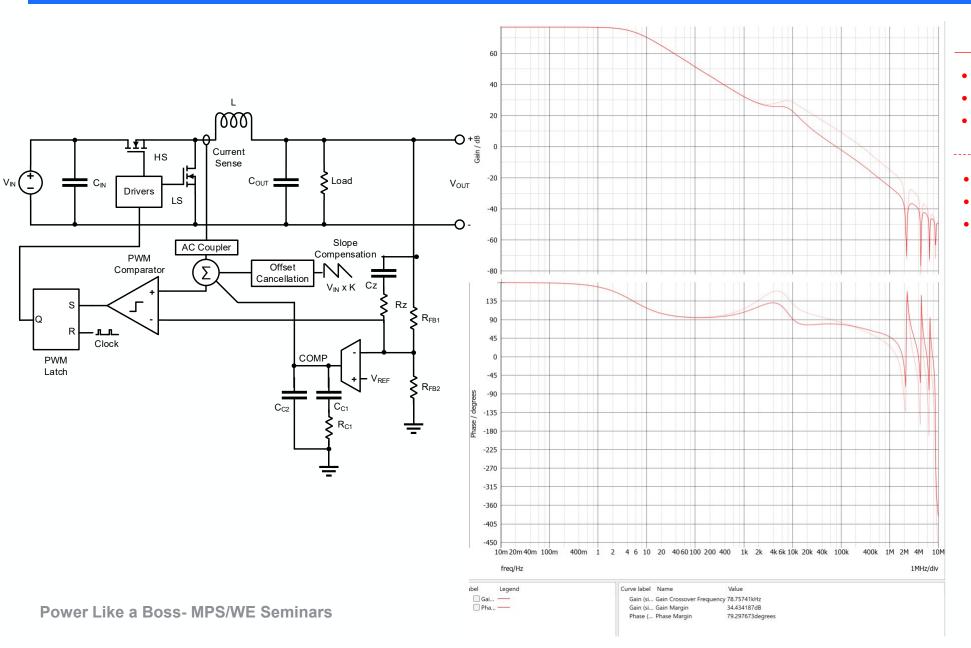
with Integrator

- Crossover Frequency: 80kHz •
- Gain Margin: 32dB .
- Phase Margin: 82 degrees •





Add Zero to Further Increase the Bandwidth



– w/o zero

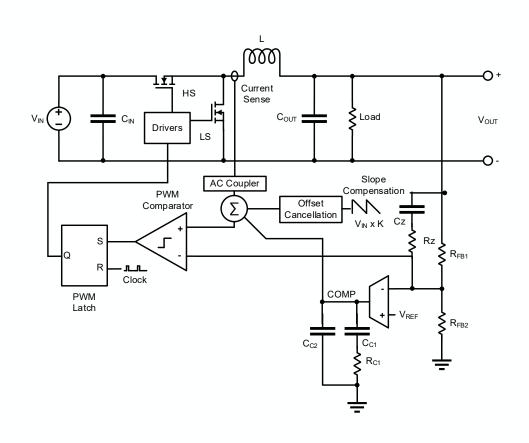
- Crossover Frequency: 80kHz
- Gain Margin: 32dB
- Phase Margin: 82 degrees

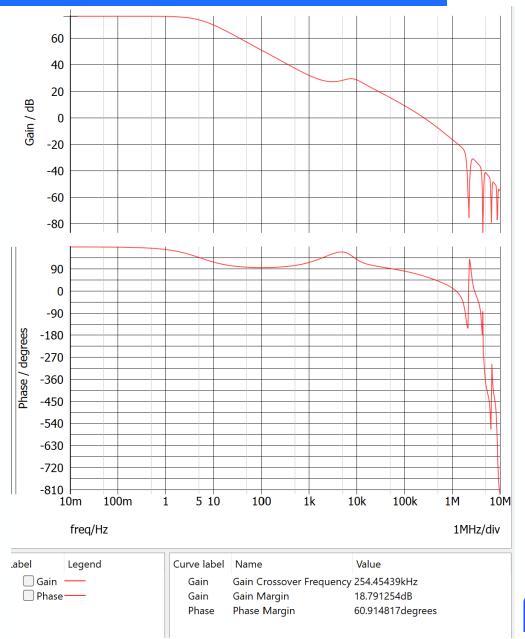
- with zero

- Crossover Frequency: 245kHz
- Gain Margin: 19dB
- Phase Margin: 61 degrees



ZDP Complete View in Frequency Domain

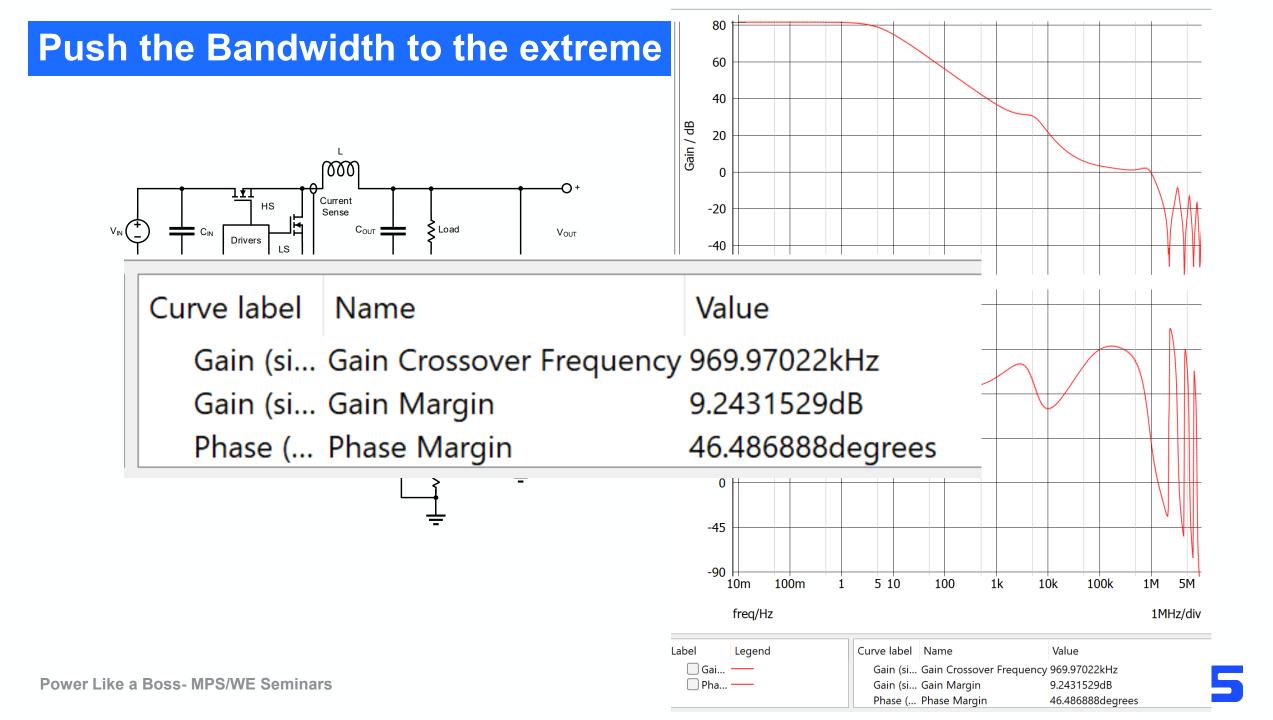




With any possible control method, the highest bandwidth a switching converter can achieve is 1/2 of switching frequency. This is limited by switcher.

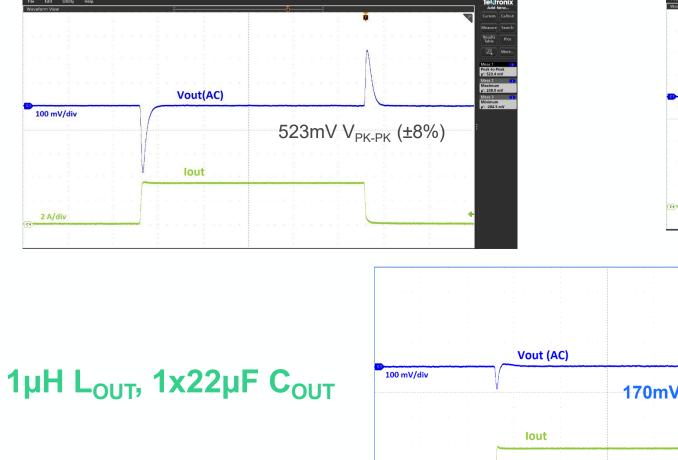
How much ZDP can achieve?





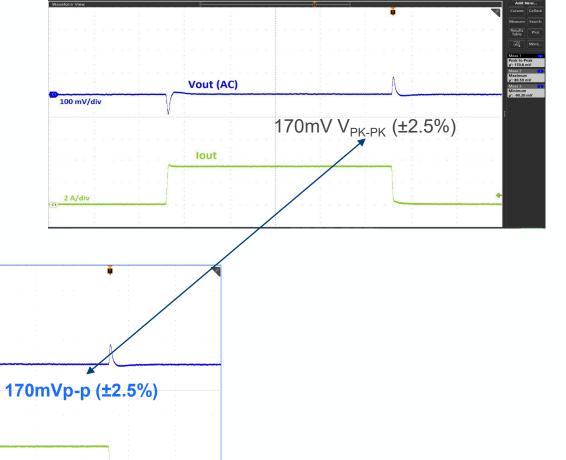
COT Advantages: Fast Transient Response

 $_{\odot}$ 12V input, 3.3V output, 0A to 3.5A load step



2 A/div

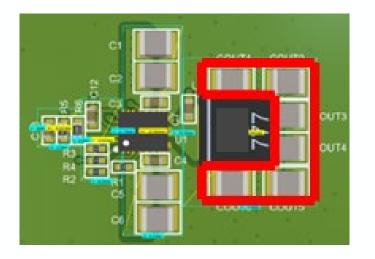
Peak Current Mode

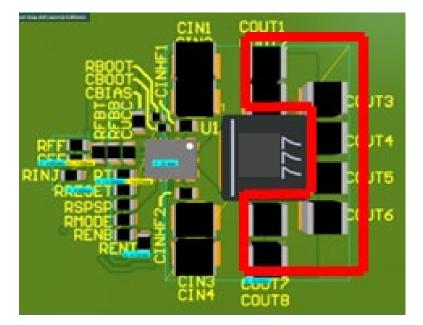


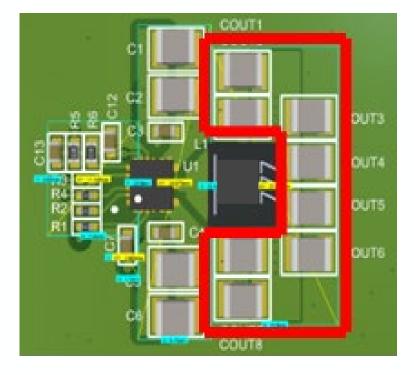
1μΗ L_{OUT}, 2x22μF C_{OUT}

Constant-On-Time

ZDP on a board







444mm²

402mm²

445mm²



Thank You

